



**DVV 3.1.3-2. Scanned images of Brochure /notification/screenshot of webpage highlighting the events attested by the Principal**

**3.1.3-CE**

**2019-20**

**1. AICTE -ISTE approved STTP Cloud Computing and High Performance Computing**

<p align="center"><b>REGISTRATION FORM</b> AICTE-ISTE Approved One Week Short Term Training program On</p> <p align="center"><b>"Cloud Computing and High Performance Computing"</b> December 09<sup>th</sup> to 14<sup>th</sup>, 2019.</p> <p>Name: _____</p> <p>Designation: _____</p> <p>Department: _____</p> <p>College: _____</p> <p>Address: _____</p> <p>Phone: _____</p> <p>Email: _____</p> <p>ISTE Membership Number: _____</p> <p>Signature of Participant _____</p> <p>Signature of Principal _____</p>	<p><b>Important Dates:</b> Registration Deadline: 5<sup>th</sup> December 2019</p> <p>Course Duration : 09<sup>th</sup> Dec to 14<sup>th</sup> Dec , 2019. ( 9.30 am to 4.30 pm )</p> <p><b>Who can apply:</b> Faculties, Research scholars and PG students from AICTE approved Engineering and Polytechnics Colleges.</p> <p>Course Fee: ( Includes certificate &amp; high ten) Rs 2000/- For ISTE Member Rs 2500/- For non ISTE Member</p> <p><b>Speakers:</b> Dr. V.B. Nikam, Professor, VJI Mumbai Dr. Rizwana Shaikh, Associate Professor, SIES GST, Nerul Dr. Bhushan Jadhav, Assistant Professor, TSEC, Mumbai. Prof. Preeti Godbole, Assistant Professor SIES GST, Nerul</p>	<p align="center">   </p> <p align="center"><b>AICTE-ISTE Approved</b> <b>Short Term Training Program</b></p> <p align="center">On</p> <p align="center"><b>Cloud Computing and High Performance Computing</b> December 09<sup>th</sup> to 14<sup>th</sup>, 2019.</p> <p align="center">Organized by</p> <p align="center"><b>Department of Computer Engineering</b></p> <p align="center"><b>SIES Graduate School of Technology</b> Sri Chandrasekarendra Saraswathy Vidyapeeth, Post-1, C.E.E, Sector-5, Nerul, Navi Mumbai 400706 Phone : 022610324910/2/2771719 <a href="http://www.siesgst.edu.in">www.siesgst.edu.in</a></p>
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Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/A/E, SIR CH. J. JOSE / ROAD, SRI CHANDRASEKHARA VIDYAPURAM  
SECTOR-5, NERUL, NAVI MUMBAI - 400706

## 2. FDP Online Teaching-Learning Practices and Tools



### FDP on Online Teaching-Learning

#### Tools & Practices

**Introduction:** The current situation has ensured that technology-based teaching/learning is gradually becoming the 'new normal'. Enhancing the skill and exploring various tools for online teaching and content development is essential.

**Objective:** To develop best practices in creating online teaching-learning content and provide overview of various open source tools. To equip in developing e-content and live streaming videos and creating interactive videos.

**Facilitator:** Faculty- SIES Graduate School of Technology (details in schedule)

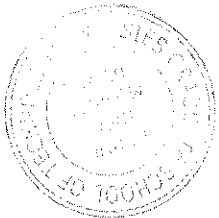
**Date & Time:** 4<sup>th</sup>, 5<sup>th</sup> & 6<sup>th</sup> June 2020 - 3.30 p.m. to 5.30 p.m.

**For Whom:** Teaching Staff

FDP will be in MS Team.

The FDP is hands-on learning hence registration is restricted to first 40 members only. kindly fill the link and complete registration process by 1<sup>st</sup> June 2020.

[https://forms.office.com/Pages/ResponsePage.aspx?id=NNsdOGDWSUa11h\\_OvhYrdjAzw-XrfRChqA63\\_km3IJUMTY1OU5YN0ZNNVRCNUVKV0IC3HINFEJRCh](https://forms.office.com/Pages/ResponsePage.aspx?id=NNsdOGDWSUa11h_OvhYrdjAzw-XrfRChqA63_km3IJUMTY1OU5YN0ZNNVRCNUVKV0IC3HINFEJRCh)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/6, SIR CANNING ROAD, CHENNAI - 600 022, DISTRICT LYAPURAM  
SECTOR 4, MERUL, HAVI NEEMBY - 600 760.

### 3. FDP Online Teaching-Learning Practices and Tools (SIES GST)



South Indian Education Society's  
GRADUATE SCHOOL OF TECHNOLOGY, Navi Mumbai.  
DEPARTMENT OF COMPUTER ENGINEERING

Faculty Development Program on  
Online Teaching-Learning Practices and Tools

Dear Concerned,

Greetings to all!!!

We are glad to inform you that we are conducting exciting Series of two days session on Online Teaching-Learning Practices and Tools

Date: 8th May '20 to 9th May '20, time: 3:30 pm to 5:00 pm

During this lockdown period these series of sessions will help you to enhance your skills in online teaching exploring various tools which will aid in this transition.

Course Objectives:

1. To teach best practices in creating online teaching-learning content.
2. To provide overview of various open source tools to create online teaching-learning content.

Course Outcomes: Faculty will be able to make use of various open source tools for creating and live streaming videos and creating interactive videos.

Course Content:

Day1:

1. Online teaching-learning content, pedagogy and assessment by Dr. Deepthi Reddy
2. Creating videos and live streaming, video transcoding Prof. Ujwala Rawale
3. Online Tools used for writing & solving mathematics. Prof. Prasad B. Iyer

Day 2:

1. Adding narrations and explaining concepts: Dr. Aparna Bammore
2. Creating Interactive videos by adding interactions in recorded videos. Prof. Pranita Mahajan
3. Post production of recorded videos (editing and adding other content) to make videos



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDBALEKJI ROAD, SIR CHANDBALEKJI VINDHYAVATI MURAM  
SECTOR 4, NAVI MUMBAI - 401 306

## 4. Workshop on Linux under NMEICT

### 4. Teaching Faculty

- Prof. Kumar Appalab, Department of Electrical Engineering, IIT Bombay
- System Administrators, FOSSEE and Spoken Tutorial, IIT Bombay

### 5. Course Fee

Every participant has to pay Rs. 100 as the course fee to IIT Bombay at the time of online registration. On the day of the workshop, i.e., on 23 August 2019, they have to pay Rs. 400 to the Remote Centre that they would be attending, to defray the cost of organising the workshop, lunch, and coffee/tea. Please note that the registration fee once paid is neither refundable nor adjustable under any circumstances.

### 6. Who should attend?

As this workshop is not subject-specific, teaching faculty from all colleges (engineering, sciences, arts, commerce, management, law, medicine, etc.), polytechnic colleges, IITs, and schools, can attend. Teachers registered in any Masters programme, and also all Research Scholars can also participate.

### 7. Criteria for Issuing Certificates

Certificate will be provided to the participants after successful completion of the workshop and filling up the feedback forms.

### 8. Duration and Venue

The workshop will be conducted on Friday, 23 August 2019 from 9.30 AM to 6.00 PM at Remote Centres of IIT Bombay. This workshop will be conducted through a blended mode, using both live video conferencing facility (A-VIEW) and hands-on sessions using Spoken Tutorials. A detailed workshop schedule will be available soon.

### 9. How to apply?

Enrollment will be strictly online, and no other mode of application will be entertained. The last date of registration

is 22 August 2019, 11:30 pm. The URL for registration is <http://www.iitb.ac.in/nmeict/announcements.html>

Register on the Spoken Tutorial Website:

1. Before the workshop participants should register on <https://spoken-tutorial.org/>
2. Fill up the registration form and submit.
3. An email will be received.
4. The account should be activated by clicking the link in the email.
5. The Username and Password should be noted. This is an extremely important step.
6. One needs the above information to post questions on the forum, explained from 8 to 9 minutes 45 seconds of [this video](#).

Note:

- Neither IIT Bombay nor the Remote Centre will bear the travel expenses of the participating representatives. There shall also be no accommodation provided to the participants.
- All participants are required to bring earphones, as they will have to listen to video tutorials. Without earphones, the workshop will not at all be effective.
- Computers to practise Spoken Tutorials and to learn Linux will be available at the Remote Centres. But if they wish, participants can bring their own laptops with Linux installed.

### 10. Address for communication

Dr. Kalpana Karmas  
Project Coordinator, ESC6 Project  
Department of CSE, Kamal Rekhi Building,  
Indian Institute of Technology Bombay,  
Mumbai - 400 076  
Tel.: +91-22-2576 4989  
Email: [projectcoordinator@iitb.ac.in](mailto:projectcoordinator@iitb.ac.in)

One day Workshop  
on  
**Linux**  
Conducted by the  
Teaching Learning Centre (TC)

Funded by the  
Pandit Madan Mohan Malaviya  
National Mission on Teachers and Teaching,  
MHRD, Govt. of India

Supported by FOSSEE & Spoken Tutorial

on

23 August 2019

Coordinated from  
IIT Bombay




Linux Course Instructor,

Prof. Kumar Appalab  
Department of Electrical Engineering,  
Indian Institute of Technology Bombay

Project Coordinator

Prof. Kannan M. Moudgalya  
Department of Chemical Engineering,  
Indian Institute of Technology Bombay  
Powai, Mumbai 400076



  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT NO. 10, SION CHAIRMAN ROAD, P. O. BOX NO. 10, SION WEST, MUMBAI - 400022  
SECTION 10, SION WEST, MUMBAI - 400022

## 1. Introduction

IIT Bombay has conducted many large-scale teacher training workshops under the Train 10,000 Teachers (T10KT) programme, sponsored by the National Mission on Education through ICT (NMEICT), MHRD, Govt. of India, and trained over 2,00,000 teachers.

Another successful technology developed at IIT Bombay is Spoken Tutorial, using which about 50 lakh students have been trained on various ICT topics. The effectiveness of this method can be seen from the testimonials available here: <https://spoken-tutorial.org/about-us/initial-successes/2015-2016/> and the TEDx talk. This project is funded by NMEICT, MHRD.

It is now proposed to offer the highly effective Spoken Tutorial based ICT training to a large number of teachers, across the country, through the T10KT methodology, with Linux training being taken up next.

Linux is one of the most popular Operating Systems used today. Linux refers to the family of Unix-like computer operating systems that use the Linux kernel. It can be installed on a wide variety of computer hardware, ranging from mobile phones, tablet computers and video game consoles to mainframes and supercomputers.

Linux is an open source software and the Linux kernel is released under the GNU General Public License and hence can be freely created, modified and redistributed.

Linux is actually just a kernel. Many people have put together distributions (often called flavors), that contain not just the kernel but also many other programming tools and utilities. Some well-known distributions include Red Hat Linux, Ubuntu, SUSE Linux, and Debian GNU/Linux.

The real power of Linux can be tapped by using its wide and powerful repertoire of commands that can be typed in the terminal. The reason behind this is the fact that Linux can trace its intellectual heritage, if not its source code, to the Unix OS, which was developed much before GUI environments were dreamt of. Thus, Unix (and hence Linux) provides a wide array of flexible text-mode commands.

Linux is now used in all major software projects undertaken by the Indian Government. Some of the examples are GST,

Aadhar, and NEFT. Naturally, the employment potential of students who are good in Linux is very high.

The FOSSEE project at IIT Bombay (<http://fossee.in>) has been promoting Linux, and other FOSS (Free/Libre and Open Source Software), such as Scilab, Python, OpenJDK, eSax, Odoo, OpenModelica, DWSEM, Drupal and R, and Open Source hardware, such as Arduino and OpenPLC. FOSSEE is also funded by NMEICT, MHRD.

The Linux workshop is organised by the Teaching Learning Centre (TLC) at IIT Bombay, funded by the Pandit Madan Mohan Malaviya National Mission on Teachers and Teaching (PMMAJNNM), MHRD, Govt. of India. As Spoken Tutorial method offers hands on practice, with 100% active learning, those who undergo this training will be able to start using Linux immediately after the workshop. They will also be able to conduct Linux workshops for their students, using Spoken Tutorials, without requiring any help from anyone else.

The one day Linux workshop will be conducted on Friday, 23 August 2019, through the select Remote Centres of IIT Bombay. This will be a blended training programme that uses A-VIEW for live interaction and Spoken Tutorials to learn to use Linux. The medium of instruction for this workshop is English.

## 2. Methodology of the Large-scale Workshop

It will be a one day workshop, on 23 August 2019. All participants will have to go to their chosen Remote Centre. They will learn Linux through the active method of reproducing every command demonstrated in the award-winning Spoken Tutorials. As learning happens through self-learning, the self-confidence of the learners is very high. Moreover, they can use the same method to train their students on the important topic of Linux, using Spoken Tutorials.

All the participants, from all the Remote Centres, will be able to interact with the Linux team at IIT Bombay, ask their questions, etc. through the video conferencing software A-VIEW. There will be about 4 to 5 hours available for Linux training and 2 hours for interactions.

A timed discussion forum (<http://forums.spoken-tutorial.org/>) will be used to answer the questions that the participants may have in different Remote Centres. One has to select the

Spoken Tutorial, and the exact time (minute and second) when the doubt arises. It will be answered immediately by one of the volunteers. Answers can also be provided by other participants in the same Remote Centre or in other Centres. These answers double as additional learning material. Course Coordinators at Remote Centres, already trained for this purpose, ensure that workshop goes through smoothly.

Using this approach, we have trained 3,500, 4,200, 4,100, and 5,300 teachers, respectively, on Koba, Moodle, Scilab, and Python, in the past 9 months.

## 3. Syllabus for the workshop

Depending on the level of the user, one can learn *any/all* of the topics mentioned below. If one cannot complete all the topics, one can complete them later on, through self-learning, as all required Spoken Tutorials are freely available.

Linux: Ubuntu Desktop, Desktop Customization, Installing software in Ubuntu Linux, Basic Commands, General Purpose Utilities, File System, Working With Regular Files, File Attributes, Redirection Pipes, Basics of System Administration.

Bash: Introduction to Bash shell scripting, Basics of Shell Scripting, Command line arguments and Quoting, Globbing and Export command, Array operations in Bash, More on Arrays, Conditional execution, Nested and multi-level if/elif statements, Logical Operators.

Linux for Sys Admin: Creating a user account, Assigning Groups on User Creation, User Password management, Modifying User Account, Group Password and Logs, Administering Group with groupadd Command, Modifying and Deleting Groups.

A Linux Virtual Box and relevant materials will be made available to all learners at the chosen Remote Centre. More Linux tutorials are available at <https://spoken-tutorial.org>.

## The FOSSEE team helps you migrate your labs

The FOSSEE team has taken up the effort to promote Linux in colleges all over India. We help you to migrate your CMT course labs to Open source software labs on the Linux Operating System. If interested, please write to us at [info@fossee.in](mailto:info@fossee.in)



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHAMBERLAIN ROAD, SATELWADY VILVAPURAM  
SECTOR 14, NAWI MUMBAI - 400 705

5. Workshop on Bootstrap



**SIES Graduate School of Technology  
Nerul, Navi Mumbai**

**Department of Computer Engineering**

Date: July 20, 2019

**Notice**

CSI -SIESGST is organizing Workshop on Bootstrap 10<sup>th</sup> August 2019. Interested students may contact CSI Council coordinators for the same. Details given below

Ms. Suvarna Chaure  
SBC, CSI-SIESGST

Dr. Aparna Bannore  
HOD



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANNARAYAN ROAD, VIDYAPURAM  
SECTOR-4, NERUL, NAVI MUMBAI - 401 007

## 6. Workshop on Python Programming




**SIES Graduate School of Technology  
Nerul, Navi Mumbai**


**Department of Computer Engineering**

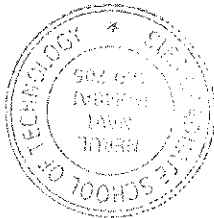
Date: August 20, 2019

### Notice

CSI -SIESGST is organizing Workshop on Basic Python Programming on 31<sup>st</sup> August 2019 at 3.30pm in Art Gallery NL2 Lab. Interested students may contact CSI Council coordinators for the same.

  
Ms. Suvama Chaure  
SBC, CSI-SIESGST

  
Dr. Aparna Bannore  
HOD





Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/E/6, SIR CHAGAPPA SWAMI MATHS, CHANDRAYAN VILVAPURAM  
SECTOR-4, NERUL, NAVI MUMBAI, MAHARASHTRA

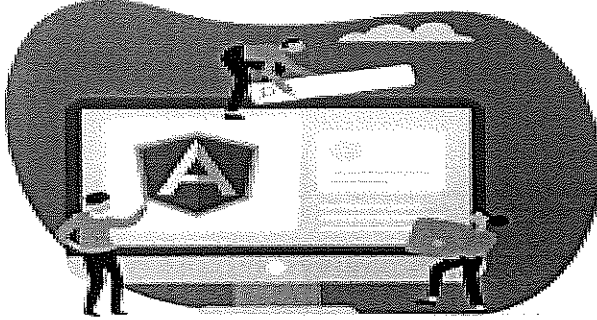
## 7. Workshop on Angular 5



SIES GRADUATE SCHOOL OF TECHNOLOGY  
PRESENTS



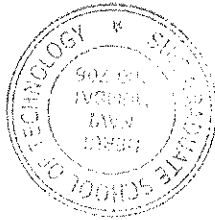
# Angular 5



Angular 5 is a JavaScript-based open-source front-end web framework mainly maintained by Google.

Speakers : Mr Vignesh Reddy  
Mr Sai prasad

Date : 12th October 2019 | Non CSI Member Entry fee : Rs 50/-  
CSI Member Entry : Free  
Jagdish Kini : 9821529349 | Varsha Kini : 9820731087



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/5, SIR C. RAJAGOPAL REDDY VIKYAPURAM  
SECTOR-V, HEERUJ, KAMPA PETA, BANGALORE



8. Session on Programming Logic building




**SIES Graduate School of Technology**  
**Nerul, Navi Mumbai**


**Department of Computer Engineering**

Date: Jan10, 2019


**Notice**

CSI -SIESGST is organizing Workshop on Programming Logic Building on 25<sup>th</sup> January 2020. Interested students may contact CSI Council coordinators for the same.


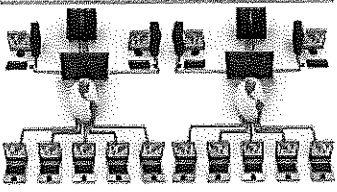
  
Ms. Suvarna Chaure  
SBC, CSI-SIESGST

  
Dr. Aparna Bannore  
HOD




  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/E, SIB CHAWANIA MARG, SION, MUMBAI 400 022  
SECTOR 10, NAVI MUMBAI - 401 208

1. AICTE-ISTE approved STTP on Computer Network and Security

<p align="center"><b>AICTE-ISTE APPROVED ONE WEEK STTP</b> on <b>"Computer Network and Security"</b> (15/11/2018 -22/11/2018)</p> <p align="center"><b>Registration Form</b></p> <p>Name: _____</p> <p>Designation: _____</p> <p>Department: _____</p> <p>College: _____</p> <p>Address: _____</p> <p>Phone: _____</p> <p>E-Mail: _____</p> <p>ISTE Member (Y/N): If yes, Membership Number: Attach the copy of proof. Amount: Rs. _____</p> <p>Draft No./Transaction ID _____</p> <p>Date: _____</p> <p>Name of the bank: _____</p> <p>Signature of Participant _____</p>	<p><b>WHO CAN APPLY:</b> Faculty from Engineering / MCA College, M.E./M. Tech students, Research Scholar.</p> <p><b>COURSE FEES:</b> Faculty Participant (ISTE member's In-house faculty): Rs. 500. PG Student Participant/Outside participant: Rs.2800</p> <p><b>REGISTRATION:</b> Participants are requested to send their registration form by email to <a href="mailto:hod_ce@siesgst.ac.in">hod_ce@siesgst.ac.in</a>.</p> <p><b>Mode of payment : Demand Draft / online</b></p> <p><b>Details of Demand Draft:</b> The Demand draft should be in favour of "Principal, SIES Graduate School of Technology" payable at Mumbai.</p> <p><b>CONTACT PERSON</b></p> <p>1) <b>Prof. Aparna Bannore</b> Mob. 9920809635 Email: <a href="mailto:bannore.aparna@siesgst.ac.in">bannore.aparna@siesgst.ac.in</a></p> <p>2) <b>Prof. Mrinal Khadke</b> Mob. 9819344929 Email: <a href="mailto:mrinal.khadke@siesgst.ac.in">mrinal.khadke@siesgst.ac.in</a></p> <p><b>Note: Non-ISTE members have to apply for ISTE life membership.</b></p>	  <p align="center"><b>AICTE-ISTE Approved ONE WEEK STTP</b> on <b>"Computer Network and Security"</b> (Nov,15-Nov 22,2018)</p> <p align="center"><b>ORGANIZED BY</b></p> <p align="center"><b>Department of Computer Engineering &amp; Department of Information Technology</b> SIES Graduate School of Technology Sri Chandrasekharendra Saraswathi Vidya puram, Plot 1-C &amp; E, Sector 5, Netaji Nayi Mumbai 400 705 Phone: 022-61082400-0227/17/19 <a href="http://www.siesgst.edu.in">http://www.siesgst.edu.in</a></p>
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**Principal**  
**SIES GRADUATE SCHOOL OF TECHNOLOGY**  
**NETAJI, SRI CHANDRASEKHARENDRASARASWATHI VIDYAPURAM**  
**SECTOR 5, NETAJI, MUMBAI-400705**

**ABOUT US**  
 SIES Graduate School of Technology, an integral part of the well-established South Indian Education Society, started in the year 2002, is located amidst the educational hub of Navi Mumbai. It is situated in congenial environment over a 6-acre plot of land at Sri Chandrasekharada Saraswathy Vidyanagar, Nerul, providing an ideal setting for academic pursuits. It is one of the premier institutes in Navi Mumbai imparting quality based Technical Education in the field of Computer Engineering, Electronics & Telecommunication Engineering, Information Technology, Printing & Packaging Technology, Biotechnology and Mechanical Engineering. The institute is approved by All India Council for Technical Education New Delhi, Directorate of Technical Education Mumbai, affiliated to the University of Mumbai, accredited by Tata Consultancy Service Limited and ISO 9001: 2008 certified. The institute has excellent infrastructure and state of the art fully equipped and sophisticated laboratories.

**COURSE OBJECTIVES:**  
 Recent developments in computer communications and networks have enabled the deployment of exciting new areas such as Internet of Things and collaborative big data analysis. The design and implementation of energy efficient future generation communication and networking technologies also require the clever research and development of mobile, pervasive, and large-scale computing technologies. Advances in Computer Communications and Networks: from Green, Mobile, Pervasive Networking to Big Data Computing require strong base in the area of networking. The growing number of the computer

Network (internet/intranet) attacks and sophistication in attack technologies has made this task still more complicated. Therefore the need of the hour is to update the knowledge on the network and security. As a result of successful completion of this workshop, participants will be able to

- Define the terminology associated with computer networks.
- Identify the components associated with computer networks.
- Develop a networking plan for yourself or a organization
- Select and configure various Open-Source tools for Network Analysis.

**SIGNIFICANCE :**  
 • Programs will be jointly certified by ISTE and AICTE.

**COURSE SCHEDULE:**  
 DATE: 15/11/2018 - 22/11/2018  
 TIME: 10:00 am -05:00 pm

**TOPICS:**  
 1) Introduction Computer Network and creating Network using Super netting and sub netting.  
 2) Configuring router and WAN interface configuration  
 3) Implementation of Default routing.  
 4) Configuring Access list.  
 5) Creating VLANs and Inter VLAN routing using L2 and L3 switches, Trunking.  
 6) Introduction to Network security.  
 7) Kali linux based tools for Security penetration  
 8) Research area in Network and Security.

**PATRONS**  
 Shri. V. Shankar  
 Hon. President, SIES.  
 Dr. Vikram Patil  
 Principal, SIESGST.

**COURSE CONVENER**  
 Dr. Rizwan Shaikh  
 Head of Department, Computer Engineering  
 Prof. K. Lakshminidha  
 Head of Department, Information Technology

**Course Co-coordinators**  
 1) Prof. Aparna Bamrore (9920809635)  
 2) Prof. Minal Khadse (Mob. 9819344929)

**Resource :**  
 1) ATS Software, Industry Professionals from Network and Security  
 2) Prof. K.Lakshminidha, Head Information Technology, IT Department, SIES GST  
 3) Prof. Azathi Boyanipalli, Asst. Professor, Computer Engineering Department, Terna Engineering College (TEC)  
 4) Prof. Gaurav Sharma, PIIT, Asst. Professor, Computer Engineering Department New Pawel

**ORGANIZING COMMITTEE:**  
 Department of Computer Engineering  
 Department of Information Technology

## 2. FDP On Networking with Linux

Mail regarding Notice to all staff members

Date :03/08/2018

Dear All,

Good Morning!

Its my pleasure to inform you all that Department of computer engineering is organizing a workshop on "Networking with Linux" on Saturday, August 04, 2018 . You all are invited to attend the same. The details are as follows :

Speaker: Mr.Ramesh Kharat (CEO, Priar Web Solutions.)

Schedule: 9:00 am -11.00 am  
 Venue: CE Lab No 07, Ground Floor

Thank You. 



  
 Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
 PLOT 104/115, SIES CAMPUS, NERUL, NAVI MUMBAI  
 SECTOR-4, NERUL, NAVI MUMBAI

### 3. FDP on Parikshak Grading tool

Fwd: FDP on "Parikshak" for CE And IT faculties



PRINCIPAL GST <principal@siesgst.ac.in>

Oct 20, 2018,  
5:33 PM

to: faculty

Dear Faculty members,

A faculty development program on Parikshak - A Program Grading tool is organised for CE and IT department faculties on 22nd October 2018 at 10 a.m. in computer centre.

You are hereby informed to attend the same

Regards  
Dr. Vikram S Patil

### 4. One day workshop on Moodle Learning Management System

#### Duration and Venue

The Coordinators' workshop will be conducted at IIT Bombay on Friday, March 1, 2019, from 9.30 AM to 6.00 PM. The workshop venue is Software Lab 201, 2nd floor, New CC Building, Adjacent to KReSIT Building, IIT Bombay, Powai - 400076.

#### Course fee

The course fee for the Coordinators' workshop is Rs. 200/-, which has to be paid at the time of online registration. The teacher and the systems administrator will have to register separately.

#### How to apply?

Enrollment will be strictly online, and no other mode of application will be entertained. The online registration for coordinators' workshop will start on 20 February 2019. The last date of registration is 28 February 2019. The URL for registration is:  
<http://www.iiitb.ac.in/news/announcements.html>

#### Register on Spoken Tutorial Website:

1. Participants should register at <https://spoken-tutorial.org/>
2. Fill up the registration form and submit
3. You will get an email
4. Activate the account by clicking the link in the email.
5. Note down your Username and Password.

#### Note

IIT Bombay will NOT bear the travel expense of the participating representatives. RCs have to bear the travel expenses.

The PMMMNMIT project will provide free accommodation, lunch and refreshments for all the participants during the workshop.

Participants are required to bring earphones, as they will have to listen to video tutorials. For those who don't

bring, low-cost earphones will be made available at the venue for a cost of Rs. 50/- each.

#### Address for communication

Dr. Kalpana Kannan  
Project Coordinator, ESOS Project  
Department of CSE, Kanwal Rekhi Building,  
Indian Institute of Technology Bombay,  
Mumbai - 400 076  
Tel: +91-22-2376 4889  
Fax: +91-22-2372 6022  
Email: [esoteach@iitb.ac.in](mailto:esoteach@iitb.ac.in)

#### One Day Coordinators' Workshop

Under 'Pankaj Madan Mohan Malaviya National Mission on Teachers and Teaching (PMMMNMTT), Funded by the Ministry of Human Resource Development, Government of India

#### Moodle Learning Management System

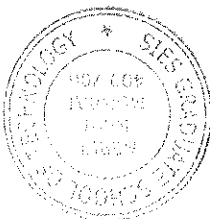
1 March 2019

Conducted by  
IIT Bombay



#### Course Coordinator

Professor Kannan Moudgalya  
Department of Chemical Engineering  
Indian Institute of Technology Bombay  
Mumbai - 400076



*(Signature)*

Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY

PLOT 104/15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100

SECTOR 4, BHANDRA, MUMBAI - 400 076

SIES GRADUATE SCHOOL OF TECHNOLOGY

INDIAN INSTITUTE OF TECHNOLOGY BOMBAY

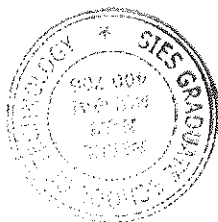
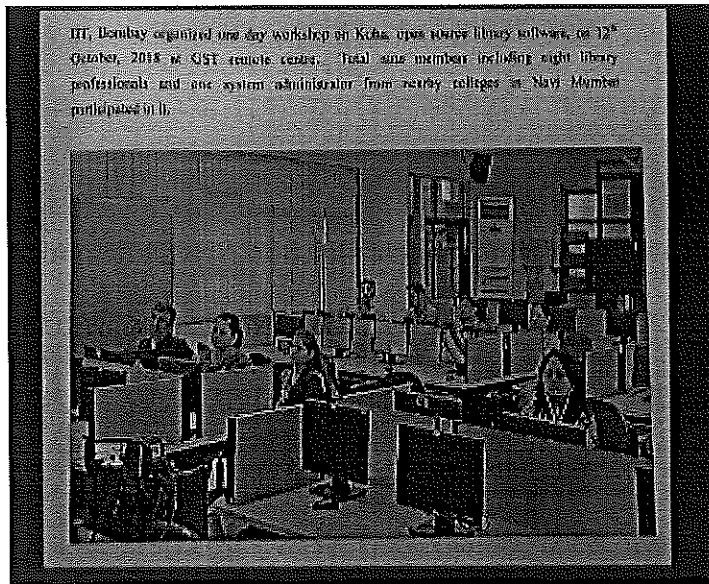
MUMBAI - 400076

**6. Workshop on KOHA and Library Automation under the National Mission on Education through ICT, MHRD, New Delhi**

**SIES GRADUATE SCHOOL OF TECHNOLOGY**

**Computer Engineering Department**

**Workshop on Koha      Date:12/10/2018**



**Principal**  
**S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY**  
PLOT 10/016, SIR CHINAI ROAD, TILAKA DEEPNAGRI VEDYAPURAM  
SECTOR-4, NEVAI, NAVI MUMBAI - 400705.

7. Workshop on Embedded systems



**SIES Graduate School of Technology**  
**Nerul, Navi Mumbai**

**Department of Computer Engineering**

Date: July10, 2018


**Notice**

CSI -SIESGST is organizing Workshop on Embedded Systems on 27<sup>th</sup> July 2018. Interested students may contact CSI Council coordinators for the same.

  
Ms. Suvarna Chaurse  
SBC, CSI-SIESGST

  
Dr. Rizwana Shaikh  
HOD



  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 108/15, SECTOR 15, NERUL, NAVI MUMBAI  
SECTOR-15, NERUL, NAVI MUMBAI

## 8. Seminar on Technical Paper Writing



**SIES Graduate School of Technology  
Nerul, Navi Mumbai**

**Department of Computer Engineering**

Date: July 10, 2018

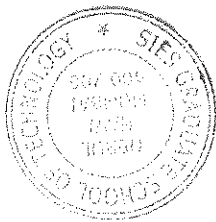
### Notice

CSI -SIESGST is organizing Seminar on Technical Paper Writing on 27<sup>th</sup> July 2018 at 3.30pm in GST Auditorium . It is Compulsory to attend for all BE (CE) Students.

Ms. SuvarnaChaure  
SBC, CSI-SIESGST

Dr. RizwanaShaikh  
HOD

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/16, SIR CHINAI ROAD, SECTOR 16, NAVI MUMBAI - 400607  
SECTOR 16, NAVI MUMBAI - 400607



9. Seminar on Industry Expectations from Engineering students



**SIES Graduate School of Technology  
Nerul, Navi Mumbai**

**Department of Computer Engineering**

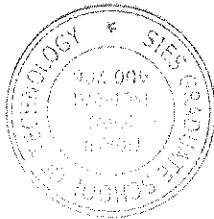
Date: July 15, 2018

**Notice**

CSI -SIESGST is organizing Seminar Industry Expectations on 1st August 2018. Interested students may contact CSI Council coordinators for the same.

Ms. Suvarna Chaure  
SBC, CSI-SIESGST

Dr. Rizwana Shaikh  
HOD



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10A/15, SION (WEST), MUMBAI - 400 022  
SECTOR 15, SION (WEST), NAVI MUMBAI - 400 022



## 10. Workshop on Android Programming

 SIES Graduate School of Technology   
CSI Student Chapter  
presents

# ANDROID APPLICATION DEVELOPMENT

on Saturday,  
August 4, 2018

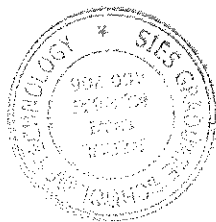


**Conducted by: Mr. Atul Jadhav,  
from 9<sup>th</sup> Legend Company**

Workshop will cover Introduction to Android Development,  
Understanding various Layout options, Activity Cycle,  
Introduction to Intent, Understanding Notifications and  
Project Based Learning.

Certificates will be provided on Completion of Project

<b>Contact:</b>	<b>Prerequisite: None</b>
Mobile: 8108206545	Fees:
Chairman: 8767274631	CSI Members: FREE
	Non-CSI Members: ₹100



*B*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/15, SIES CAMPUS, SION, MUMBAI 400 022  
SECTOR 16, HEAL, NAVI PUNE - 400 705

## 11. Seminar on Codechef Activities




**SIES Graduate School of Technology  
Nerul, Navi Mumbai**

**Department of Computer Engineering**

Date: August 01, 2018


### Notice

CSI-SIESGST is organizing Seminar on Codechef Activities on 14<sup>th</sup> August 2018 at 3.30pm in GST Auditorium .Interested students may contact CSI Council coordinators for the same.

  
Ms. Suvarna Chaure  
SBC, CSI-SIESGST

  
Dr. Rizwana Shaikh  
HOD



  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/A/E, SIA CHANDRANAGAR, VILAS RASTHY VILLAGES  
SECTOR-4, NERUL, NAVI MUMBAI - 401 006.

## 12. Seminar on Smart India Hackathon



**SIES Graduate School of Technology  
Nerul, Navi Mumbai**

**Department of Computer Engineering**

Date: October 10, 2018

### Notice

CSI -SIESGST is organizing Seminar on Smart India Hackathon on 10<sup>th</sup> October 2018. Interested students may contact CSI Council coordinators for the same.

Ms. Suvarna Chaure  
SBC, CSI-SIESGST

Dr. Rizwana Shaikh  
HOD

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/WE, SIR CHATEL ROAD, S. D. J. DEWATHY VIDYAPURAM  
SECTOR 4, NERUL, NAVI MUMBAI - 401 208.



## 14. Seminar on Codechef Activities




**SIES Graduate School of Technology  
Nerul, Navi Mumbai**

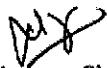
**Department of Computer Engineering**

Date: August 01, 2018

### Notice

CSI -SIESGST is organizing Seminar on Codechef Activities on 14<sup>th</sup> August 2018 at 3.30pm in GST Auditorium .Interested students may contact CSI Council coordinators for the same.

  
Ms. Suvarna Chaurse  
SBC, CSI-SIESGST

  
Dr. Rizwana Shaikh  
HOD



  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 30/31E, SAKUBHINDRASEKHAR ROAD, SECTOR 15, NAVI MUMBAI - 401 106

## 15. Workshop on Latex




**SIES Graduate School of Technology  
Nerul, Navi Mumbai**


**Department of Computer Engineering**

Date: April 01, 2019


### Notice

CSI -SIESGST is organizing Workshop on Latex on 3<sup>rd</sup> April 2019. Interested students may contact CSI Council coordinators for the same.

  
Ms. Suvarna Chaure  
SBC, CSI-SIESGST

  
Dr. Rizwana Shaikh  
HOD



  
PRINCIPAL  
SIES GRADUATE SCHOOL OF TECHNOLOGY  
PLOT NO. 16, SIES CAMPUS, NERUL, NAVI MUMBAI - 401 007  
SECTIONAL OFFICE, NAVI MUMBAI - 401 007

**1. STTP on Cyber Security and Digital Forensic**

**AICTE-ISTE APPROVED ONE WEEK STTP**  
on  
**"Cyber Security and Digital Forensics"**  
(July 03 - July 8, 2017)

**Registration Form**

Name: \_\_\_\_\_  
 Designation: \_\_\_\_\_  
 Department: \_\_\_\_\_  
 College: \_\_\_\_\_  
 Address: \_\_\_\_\_  
 Phone: \_\_\_\_\_  
 E-Mail \*: \_\_\_\_\_

ISTE Member (Y/N): \_\_\_\_\_  
 If yes, Membership Number: \_\_\_\_\_  
 Attach the copy of proof.  
 Amount: Rs. \_\_\_\_\_  
 Draft No/Transaction ID: \_\_\_\_\_  
 Date: \_\_\_\_\_  
 Name of the bank: \_\_\_\_\_

Signature of Participant \_\_\_\_\_

Signature of Principal \_\_\_\_\_

\* Details for online payment

AAC Name	SIES Graduate School of Technology
AAC No	057200101003139
IFSC Code	CORP0000872
Branch	Corporation Bank, Nerul, Navi Mumbai

**WHO CAN APPLY:**  
 Faculty from Engineering / MCA College,  
 M.E./M.Tech students, Research Scholar.

**COURSE FEES:**  
 Faculty Participant(ISTE members): Rs. 2000  
 PG Student Participant : Rs.2200

**REGISTRATION:**  
 Participants are requested to send their registration form by email to [hod\\_ce@siesgst.ac.in](mailto:hod_ce@siesgst.ac.in) on or before 02/07/2017.

**Mode of payment :** Demand Draft / online


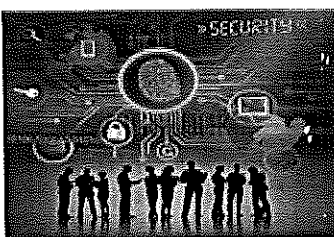
**Details of Demand Draft:**  
 The Demand draft should be in favour of "Principal, SIES Graduate School of Technology" payable at Mumbai.

**CONTACT PERSON**

1) Prof. Aparna Bannore  
 Mob. 9920809635  
 Email: [bannore.aparna@siesgst.ac.in](mailto:bannore.aparna@siesgst.ac.in)

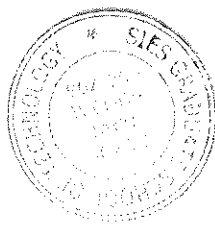
2) Prof. Ujjwala Ravale  
 Mob. 9930805236  
 Email: [ujwala.ravale@siesgst.ac.in](mailto:ujwala.ravale@siesgst.ac.in)

**Note:** Non-ISTE members have to apply for ISTE life membership.

**AICTE-ISTE Approved ONE WEEK STTP**  
on  
**"Cyber Security and Digital Forensics"**  
(July 03 - July 8, 2017)

**ORGANIZED BY**  
 Department of Computer Engineering  
 SIES Graduate School of Technology  
 Sri Chandrasekarendra Saraswathy  
 Vileya parani, Plot 1-C & E, Sector 3, Nerul  
 Navi Mumbai 400 706  
 Phone:  
 022-61082400/0227171719  
<http://www.siesgst.edu.in>



*(Signature)*  
 Principal  
 S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
 PLOT 1C/E, SRI CHANDRASEKARENDRASARASWATHY  
 SECTOR 3, NERUL, NAVI MUMBAI - 400706

**ABOUT US**  
 SIES Graduate School of Technology, an integral part of the well-established South Indian Education Society, started in the year 2002, is located amidst the educational hub of Navi Mumbai. It is situated in congenial environment over a 6-acre plot of land at Sri Chandrasekarendra Saraswathi Vidyapuram, Nerul, providing an ideal setting for academic pursuits. It is one of the premier institutes in Navi Mumbai imparting quality based Technical Education in the field of Computer Engineering, Electronics & Telecommunication Engineering, Information Technology, Printing & Packaging Technology, Biotechnology and Mechanical Engineering. The Institute is approved by All India Council for Technical Education New Delhi, Directorate of Technical Education Mumbai, affiliated to the University of Mumbai, accredited by Tata Consultancy Service Limited and ISO 9001:2008 certified. The institute has excellent infrastructure and state of the art fully equipped and sophisticated laboratories.

**COURSE OBJECTIVES:**  
 In today's networked world, most of the organizations and enterprises depend on different kinds of Information Technology solutions. The growing number of the computer Network (internet/intranet) attacks and sophistication in attack technologies has made this task still more complicated. Therefore the need of the hour is to update the knowledge on the network security and digital forensics issues and solutions. As a result of successful completion of this workshop, participants will be able to

- Understand current cyber security threats

- Analyze traffic patterns associated with suspicious network behavior
- Understand the principles of Network Forensics Analysis and how to apply them
- Select and configure various Open-Source tools for Network Forensics Analysis.

**SIGNIFICANCE :**

- Programs will be jointly certified by ISTE and ACCTE.
- Program will be considered for the Career Advancement Scheme of ACCTE.

**COURSE SCHEDULE:**  
**DATE: 23/07/2017 -08/07/2017**  
**TIME: 10:00 am -05:00 pm**

**TOPICS:**

- 1) Introduction to Cyber Security:
  - Security Threats
  - Network Scanning, Probing, and Sniffing
- 2) Penetration Testing Tools:
  - Network Analysis Tools
  - DDOS attack
- 3) Building Firewall and IDS system
- 4) Cyber Security
  - Cyber-crime and Cyber terrorism
  - Investigation Tools
- 5) Digital Forensic Tools
- 6) Virtual/Cyber Money
- 7) Network Forensic Investigation and Tools
- 8) Network, Android, Mobile Phone Forensics
- 9) IoT & Cloud Security.
- 10) Open web Application Security Projects

**PATRONS**

Shri. V. Shankar  
 Hon. President, SIES.

Shri. S. Ganesh  
 Hon. Secretary, SIES.

Dr. Vikram Patil  
 Principal, SIESGIST.

**COURSE CONVENER**  
 Dr. Rizwana Shaikh  
 Head of Department, Computer Engineering

**Course Co-coordinators**  
 1) Prof. Aparna Bannore (9920809635)  
 2) Prof. Ujjwal Ravale (9930805236)

**EMINENT SPEAKERS :**

- Prof. Atul Kahate, Professor, Pune University and Symbiosis International University
- Dr. Madhumita A Chatterjee, Professor, Department of Computer Engineering, PBT, Navi Mumbai
- Prof. Dayanand Ambawade, Associate Professor, Department of IEXTC, SPIT, Mumbai
- Dr. Satish Devane, Professor, Information Technology, DMCE, Navi Mumbai.
- Dr. Jyoti Rao, Associate Professor, Computer Engineering DYPIET, Pimpri, Pune.

Industry Professionals from Security and forensic area

**ORGANIZING COMMITTEE:**  
 Department of Computer Engineering

## 2. Two Day Weekend Training Program-Saptahaant Shikshak Prashikshan(SSP)on Introduction to Machine Learning



### Fwd: Two Days Weekend Faculty Development Programme - Saptahaant Shikshak Prashikshan (SSP) on "Introduction to Machine Learning"

1 message

bannore.aparna <bannore.aparna@siesgst.ac.in>  
 To: Pranita Mahajan <pranitamahajan@siesgst.ac.in>

Mon, 27 Nov, 2017 at 2:56 PM

link for registration

<http://www.nmeict.iitkgp.ernet.in/sspml.php>

----- Forwarded message -----

From: HOD CE <hod\_ce@siesgst.ac.in>

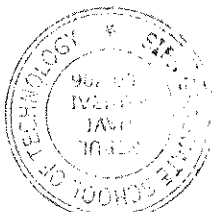
Date: Mon, Nov 27, 2017 at 12:47 PM

Subject: Fwd: Two Days Weekend Faculty Development Programme - Saptahaant Shikshak Prashikshan (SSP) on "Introduction to Machine Learning"

To: bannore.aparna <bannore.aparna@siesgst.ac.in>

----- Forwarded message -----

From: ICT Education IIT Kharan <office\_nmeict@iitkgp.ac.in>



Principal  
 S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
 PLOT 104/5, SIR CHANDRASEKARENDRA SARASWATHI VIDYAPURAM  
 SECTOR-08, NERUL, NAVI MUMBAI - 401 007





#### 4. Workshop on Image Processing using Python




**SIES Graduate School of Technology  
Nerul, Navi Mumbai**

**Department of Computer Engineering**

Date: Sep 01, 2017


#### Notice

CSI -SIESGST is organizing Workshop on Python Programming from 4<sup>th</sup> Sep to 14<sup>th</sup> sep 2017. Interested students may contact CSI Council coordinators for the same.

  
Ms. Suvarna Chaure  
SBC, CSI-SIESGST

  
Dr. Rizwana Shaikh  
HOD



  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/1/E, SIR CHANDRASEVA PONDRA DALASWATHY VIDYAPURAM  
SECTOR-4, NERUL, NAVI MUMBAI - 406 706

## 5. PC Assembling Workshop



**SIES Graduate School of Technology  
Nerul, Navi Mumbai**

**Department of Computer Engineering**

Date: Sep 14, 2017

### Notice

CSI -SIESGST is organizing Workshop on PC Assembly on 22<sup>nd</sup> Sep2017. Interested students may contact CSI Council coordinators for the same.

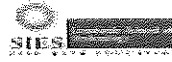
Ms. Suvarna Chaure  
SBC, CSI-SIESGST

Dr. Rizwana Shaikh  
HOD



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/5, SIR CHANDRASEKHAR PURA SARASWATI VIDYAPURAM  
SECTOR-4, NERUL, NAVI MUMBAI - 400 706.

## 6. Student Development Programme on Basic Linux & LaTeX



### Department of Computer Engineering

#### Student Development Programme on LAMP

20-24 Dec 2017

The department is announcing student development program from 20-24 DEC 2017 specifically designed for **Second year CEE/IT/EC** students. Interested students can contact Prof. Namrata Patel

#### OBJECTIVES

1. Learn Fundamentals of Linux and Networking Commands
2. Understand how server-side programming works on the web
3. Create a database in PhpMyAdmin
4. Create a Website

#### WHO WILL BE BENEFITED

1. Students who want to develop an Web Application on Linux Platform
2. Students would be benefited from learning Linux and its features for embedded system

#### PREREQUISITE

Basics of HTML.

#### OUTCOMES

Learner will be able to:

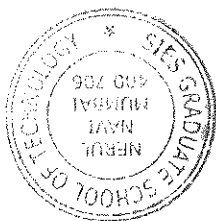
1. Understand Installation Procedure of Linux OS
2. Basic and Advance Commands in Linux OS
3. Install Apache server and MySQL on Linux OS
4. Develop an end-to-end application in PHP

#### CONTACT PERSON

Prof. Namrata Patel  
[namrata.patel@siesgst.ac.in](mailto:namrata.patel@siesgst.ac.in)

9820919322

Workshop is free of cost.



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, GIR CHANDPASEKHANDEWADA, BARASAMATHY, VEENAPURAM  
SECTOR-4, NERUR, NAVI MUMBAI - 401 705.

2016-17

## 1. Workshop on Android Programming

16-17


**SIES Graduate School of Technology**  
**Department of Computer Engineering**

Date: July 16, 2016

### CSI Notice

CSI SB of SIESGST is organizing Workshop on Android programming on July 30 & 31, 2016.  
Mr. Atul from 9<sup>th</sup> legend is eminent speaker.

Interested students may contact CSI Council coordinators for the same.

  
Ms. Suvama Chaur

SBC, CSI-SIESGST student branch.

  
Dr. Rizwana Shaikh

Head of CE Department



  
Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDRASEKHARJIJI SARASWATHY VILASANIPAM  
SECTOR-4, MERUL, NAVI MUMBAI - 401 201.

## 2. ISTE approved workshop on Mobile Communication and Android Programming

<p>Name: _____</p> <p>Address: _____</p> <p>Phone: _____</p> <p>Mobile: _____</p> <p>Signature: _____</p> <p>Date: _____</p> <p>For additional copies please see photographs of this device.</p>	<p><b>REGISTRATION</b></p> <p>Participants are requested to send their registration form to _____</p> <p><b>CONFERENCE</b></p> <p>1. Prof. Anand K. Patil and Dr. Chandrasekhar S. Patil 2. Prof. Manoj K. Patil and Dr. Chandrasekhar S. Patil</p>	<p><b>ISTE APPROVED</b></p> <p><b>ONE WEEK STEP</b></p> <p><b>"Mobile Communication &amp; Android Programming"</b></p> <p>(From 1<sup>st</sup> to July 1<sup>st</sup>, 2016)</p> <p>FOR REGISTRATION FOR FURTHER COPY CONTACT THE ISTE IN CHARGE OF REGISTRATION UNIT ADDRESS: 10/11/12, SECTOR-4, NAVI MUMBAI - 400 082</p>
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*[Handwritten Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/11/12, SIR CHANDRASEKHAR S. PATIL COLONY, SECTOR-4, NAVI MUMBAI - 400 082

3. Workshop on Document management system :LaTex

CE 8

**One day Workshop on LaTeX:Document Preparation System**

**Inbox x**

office <office\_supt@siesgst.ac.in>

1/5/  
17

to faculty\_gst

Dear All,

Good Morning!

Its my pleasure to invite you all for a one day workshop on "LaTeX:Document Preparation System" on January 6 (Friday), 2017 organized by SIES GST Workshop Committee .

Speaker :Dr.Sanjay Shitole

Schedule : 9.30 am to 10.30 am - Introductory Session in GST Auditorium  
10:45 am onwards -Art Gallery

Ms Aparna M Bannore



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDRASEKHARJI ROAD, WADYAPURAM  
SECTOR 4, NERUL, NAVI MUMBAI - 401 105.

## 4. Workshop on Python

**SIES Graduate School of Technology**  
**Department of Computer Engineering**

Date: July 22, 2017

### CSI Notice

SI SB of SIESGST is organizing Workshop on Python programming on Jan 28, 2017. Mr. Lip from Seven Technology is eminent speaker.

Interested students may contact CSI Council coordinators for the same.



Urvana Chaurse

CSI-SIESGST student branch.



Dr. Rizwana Shaikh

Head of CE Department



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/16, SIR CHANDRASEKHARJI, SARASWATHI VIKRAMPURAM  
SECTOR 8, MIDC, TAITI MUMBAI - 400 716.



## 5. Student Development Programme on Basic Linux and LaTeX

### Student Development Program on Linux and Lamp

19<sup>th</sup> – 23<sup>rd</sup> June 2017

CE department

CE department is announcing student development program from 19<sup>th</sup> June to 23<sup>rd</sup> June 2017, specifically designed for Second year CE/IT pass-out students. Interested students can contact Prof. Namrata Patel ([namrata.patel@siesgst.ac.in](mailto:namrata.patel@siesgst.ac.in))

Who will be benefited?

1. Students who want to develop an Web Application on Linux Platform
2. Students would be benefited from learning Linux and its features for embedded system

Prerequisite

Basics of HTML and DBMS

By the end of the course, learner can:

Understand Installation Procedure of Linux OS, and other applications.

Linux commands and Shell Scripting

Php and Mysql Connectivity and configuration

Develop an end-to-end application



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/5, SIR CHANDRASEKHAR ROAD, SARAKATHELY, VEZAPURAM  
SECTOR-4, WARD-1, NAVI MUMBAI - 400 074.



2015-16

1. Workshop on Linux

SIES Graduate School of Technology  
Department of Computer Engineering

15.16

Date: July 16, 2015

CSI Notice

CSI SB of SIESGST is organizing Workshop on Linux on Aug 1st, 2015. Interested students may contact CSI Council coordinators for the same.



Ms. Suvama Chaure  
SBC, CSI-SIESGST student branch.



Prof. Rajesh Kadu  
Head of CE Department



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHATDRASEKARILIN, SRI SWATHY VIDYAPURAM  
SECTOR-4, MIDC, NAVI MUMBAI - 401 705.



## 2. Workshop on Core Java




**SIES Graduate School of Technology  
Nerul, Navi Mumbai**


**Department of Computer Engineering**

Date: August 16, 2015


### Notice

CSI -SIESGST is organizing Workshop on Core Java on 25<sup>th</sup> August 2015.  
Resource person for the workshop is Mr. Vivek Saxena , NIIT .Interested students  
may contact CSI Council coordinators for the same.

  
Ms. Suvarna Chaure  
SBC, CSI-SIESGST

  
Dr. Rizwana Shaikh  
HOD



  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/E, SIR CHAITANVAS KRISHNAJI, CHANDANWADI, MIDC, NERUL  
SECTOR-4, NERUL, NAVI MUMBAI - 401 001

3. Workshop on Advanced Java




**SIES Graduate School of Technology**  
**Nerul, Navi Mumbai**

**Department of Computer Engineering**

Date: August 16, 2015


**Notice**

CSI -SIESGST is organizing Workshop on Advanced Java on 24<sup>th</sup> August 2015. Resource person for the workshop is Mr. Vivek Saxena , NIIT .Interested students may contact CSI Council coordinators for the same.

  
Ms. Suvarna Chaure  
SBC, CSI-SIESGST

  
Dr. Rizwana Shaikh  
HOD



  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/7E, SIR CHANDRASEKHARAPURAS WADIA MATHY VIDYAPURAM  
SECTOR-4, NERUL, NAVI MUMBAI - 400 761.

4. Workshop on Beautiful Coding



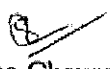
**SIES Graduate School of Technology**  
**Nerul, Navi Mumbai**


**Department of Computer Engineering**

Date: January 10, 2016


**Notice**

CSI -SIESGST is organizing Workshop on Beautiful Coding on 22<sup>nd</sup> Jan 2016.  
Resource person for the workshop is Mr. Joel J. Luis, Mastek. Interested students  
may contact CSI Council coordinators for the same.

  
Ms. Suvarna Chaure  
SBC, CSI-SIESGST

  
Dr. Rizwana Shaikh  
HOD



  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/5, SIR CHANDRASEKHAR NERDESA SMARAKAITHY VIDYAPURAM  
SECTOR-V, NERUL, NAVI MUMBAI - 401 706

## 5. Microsoft Azure Campus Training Workshop



**SIES Graduate School of Technology  
Nerul, Navi Mumbai**

**Department of Computer Engineering**

Date: February 10, 2016

### Notice

CSI -SIESGST is organizing Microsoft Azure Campus Training Workshop on 18<sup>th</sup> Feb 2016. Resource person for the workshop is Mr. Hardik Mistry, Appmatic. Objective of this workshop was to give brief about cloud services using Microsoft Azure framework. Interested students may contact CSI Council coordinators for the same.

Ms. Suvarna Chaure  
SBC, CSI-SIESGST

Dr. Rizwana Shaikh  
HOD

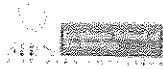


Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/5, SIA CHANDRASEKHAR/DRA SUJASWATHY VIDYAPURAM  
SECTOR-V, LERUL, NAVI MUMBAI - 400 705.

## 6. Three days workshop on Big Data and Hadoop

<mailto:ce@csiegsst.ac.in>

CSIEGSST Mail - STTP on big data and hadoop



HOD CE <hod\_ce@csiegsst.ac.in>

### STTP on big data and hadoop

HOD CE <hod\_ce@csiegsst.ac.in>

Mon, Jun 22, 2015 at 4:22 PM

To: Apama Kale <apamakale96@gmail.com>, "deepthi.reddy" <deepthi.reddy@csiegsst.ac.in>, Rizwana Shaikh <rizwana.shaikh@csiegsst.ac.in>, SUNIL PUNJABI <sunil.punjabi@csiegsst.ac.in>, "suvarna.kendre" <suvarna.kendre@csiegsst.ac.in>, prachi.shahane@csiegsst.ac.in, Pranita Mahajan <pranita.mahajan@csiegsst.ac.in>, Ujjwala Ravale <ujwala.ravale@csiegsst.ac.in>, Preeti Godabole <preeti.godabole@csiegsst.ac.in>, Varsha Patil <varsha.patil@csiegsst.ac.in>, Ingledarshan@csiegsst.ac.in, Ravindra Agrawal <rragraval1023@gmail.com>, masooda.modak@csiegsst.ac.in, MONIKA SHARMA <monika.sharma@csiegsst.ac.in>, Roopal Mamtara <roopal.mamtara@gmail.com>, asmita.shejale@csiegsst.ac.in, namrata.patel@csiegsst.ac.in, Anindita Khade <aninditaac1987@gmail.com>, Kranti Jaybhay <kranti99@gmail.com>, "kalyani.pampattiwar" <kalyani.pampattiwar@csiegsst.ac.in>

Dear All,

Department is organizing three days STTP on "Big Data and Hadoop" on 07/07/15, 09/07/15 and 10/07/15. The course contents are designed in keeping view the VIII Semester Elective Subject syllabus of Mumbai University and will be conducted in Flipped classroom mode. Faculty will be preparing theoretical concepts before commencing of the training program. There will be hands on sessions on all three days. Detailed schedule and course contents of the workshop will be specified shortly. Faculty are requested to contribute Rs.2300 towards registration fees of the STTP. The course is aimed at imparting the knowledge to the faculty on latest technology. Registration for the STTP is compulsory for all the faculty members of Computer Engineering department.

Resource person: Mr. Shrikant Gawande, Director Peta Bytes

Course coordinator: Aparna Bannore.

Kindly register before 30/06/2015.

Regards



CSIEGSST  
C.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
401, PETA BYTES, 5th FLOOR, PETA BYTES, PETA BYTES, PETA BYTES  
MUMBAI, INDIA

7. Workshop on Microsoft Azure Technology

SIES GRADUATE SCHOOL OF TECHNOLOGY  
Computer Engineering Department  
NOTICE

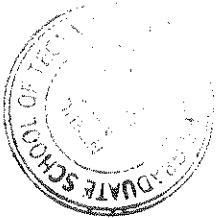
16/2/2016

Computer Department of SIESGST is organizing a FDP on Microsoft Azure Technology by Mr. Hardik Mistry. This workshop is scheduled on 18<sup>th</sup> Feb 2016 at Lab computer Center third floor from 9.30am onwards.

Faculty members are requested to attend the same.

  
Prof. Rizwana Shaikh

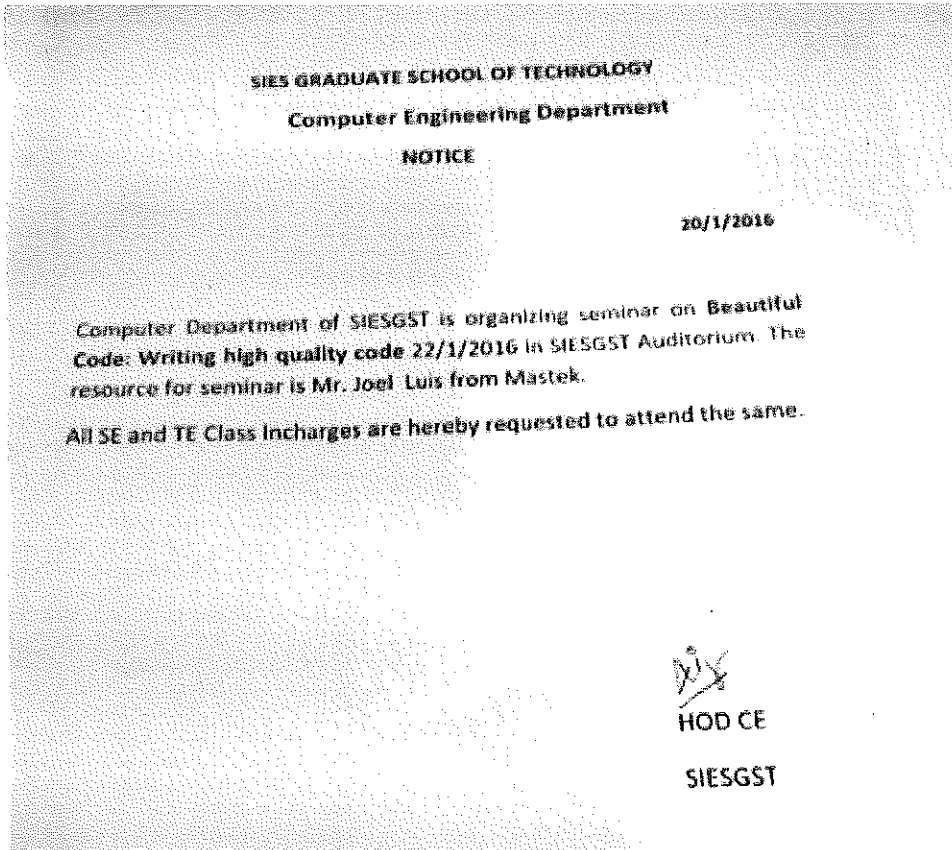
HOD CE





Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDRASEKHAR DADA SARASWATHY VIDYAPURAM  
SECTOR-4, MUMBAI, NAVI MUMBAI - 400 705.

## 8. Workshop\_Beautiful Coding



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/15, SIR CHANDESHWARJI ROAD, NEW ANANDHY VILAPURHAWA  
SECTOR-4, HSRULI, NAVI MUMBAI - 400 705.



## 9. Seminar on Agile Technology

SIES GRADUATE SCHOOL OF TECHNOLOGY

Computer Engineering Department

NOTICE

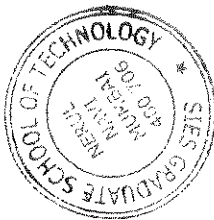
14/9/2015

Computer Department of SIESGST is organizing seminar on Agile Technology on 16/9/2015 in computer lab 07. The resource for seminar is Mr. Aniket Mhala global agile leader.

All are hereby requested to attend the same.

  
HOD CE

SIESGST





Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10A/15, SBI COLONY, WADGAON, PUNE-411 004 (WADGAON VIDYAPURAM)  
SECTOR-4, AREA - 10/VI, MIDC, PUNE-411 004

3.1.3\_EXTC\_Year 2019-20

Serial No.10

Event: EXTC WIE Seminar

Date: 01-08-2019

Resource Person: Prof.Seema Kawale, WIE chair, IEEE Bombay Section

Nature: Regional



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 3, Nerul, Navimumbai-400706



NOTICE

29-07-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a seminar on Women in Engineering (WIE) for you. The interested students can register for the same. The event details are given below.

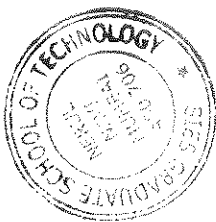
Name of the event	Seminar on Women in Engineering at GST Audi
Date	01-08-2019, 3.30 PM
Eligibility	Open to all students
Contact Person	Jayji Joshi 7506486269 Supra Sharma 9987288306

*Rg Bsb*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N.Kemkar*

Dr. Atul Kemkar  
HOD, Dept of EXTC



*[Signature]*  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/WE, SRI CHANDRASEKARENDRASARASWATHI VIDYAPURAM  
SECTOR-3, NERUL, NAVI MUMBAI - 400 706.

**Serial No.11**

**Event: Python Workshop**

**Date: 07-08-2019 ,9-8-1019 , 13-8-2019 ,14-8-2019**

**Resource Person: Google Certified Technical Team of IEEE SIESGST Student Branch**

**Nature: Regional**

1



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



**NOTICE**

01-08-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on Python Programming for you. The interested students can register for the same. The event details are given below.

Name of the event	Python Programming
Date	07-08-2019, 09-08-2019, 13-08-2019, 14-09-2019, 3.30 PM
Eligibility	Open to all EXTC students
Contact Person	Abhishek Anand 9769011702 Sriharsha 8976706250

*Dr Biju*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
Head, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/10/6, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM,  
SECTOR 5, NERUL, NAVIMUMBAI-400706

**Serial No.12**

**Event:-**Digital Marketing Workshop

**Date:** 10-08-2019

**Resource Person:** Ms.Priya Singh, GM, Corporate Infocom Pvt Ltd. and Digital Marketing Trainer

**Nature: Regional**



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



### NOTICE

05-08-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on Digital Marketing for you. The interested students can register for the same. The event details are given below.

Name of the event	Digital Marketing workshop by Ms. Priya Singh
Date and Time	10-08-2019, 10 AM to 4 PM
Eligibility	Open to all students
Contact Person	Abhishek Singh 8425877338 Sahil Pandita 7780871070

*RG BSB*

Prof. Biju Balakrishnan

IEEE SB Branch Counselor

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC

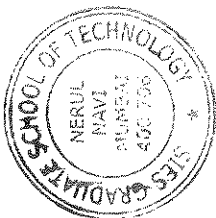
**Serial No.13**

**Event :** Amazon Alexa Workshop

**Date:** 31-08-2019

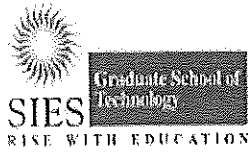
**Resource Person :** Mr. Hannan Satopay, AutoDesk Tinkercad Advisor and Alexa Developer, IEEE Bombay Section

**Nature :** Regional



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/15, SRI CHANDRASEKARENDA SARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVI MUMBAI - 400 706.



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

26-08-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing Amazon Alexa Workshop for you. The interested students can register for the same. The event details are given below.

Name of the event	Amazon Alexa Workshop
Date	31-08-2019 at CC, 10 AM to 5 PM
Eligibility	Open to all students
Contact Person	Sricharan 8976706250

RG 109/16

Prof. Biju Balakrishnan

IEEE SB Branch Counselor



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 109/16, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM, SECTOR 5,  
NAVIMUMBAI - 400706

Serial No.14

Event : Workshop on Design and Testing of Microwave Components using IE3D

Date:31-08-2019

Resource Person : Prof. Shishir Jagtap Prof. Vandana Sawant, Faculty, SIESGST

Nature : Regional



SIES Graduate School of Technology  
Department of Electronics and Telecommunication Engineering  
IEEE SIES GST Student Branch and IETE Chapter Project

Workshop on

**Design and Testing of Microwave Components using IE3D**

August 31, and September 7<sup>th</sup> of 2018 Timing :10.00 AM to 4.00 PM, Venue :Lab 316

Since the inception of the Microwave/Microwave Component four decades ago, the demand for its application has been increasing rapidly, especially within the last two decades. However, these applications have been in demand mostly by the Superpowers of Defense. Because of their sensitivity (in terms of Q/F) to GHz wavelength, present Microwave/Microwave Components have found heavy applications in military systems, missiles, radars, and satellites. The Microwave/Microwave Components because of its small size, lightweight, low profile, and low manufacturing cost, is finding increasing applications in the commercial sector of the industry. This workshop aims at discussing briefly some of the component's mechanical design features and present several temperature sensitive of the commercial applications, such as satellite communication, direct broadcast satellite services, global positioning systems, medical hyperthermia therapy, etc.

**Objectives**

- Introduction to basic understanding and designing of the Microwave Component.
- Simulation of the Microwave Component using simulation software IE3D.
- To analyze, design and temperature difference types of power divider, Tee, Hybrid ring, Directional Coupler suitable for numerous applications (in microwave communication, radar, RFID applications, etc).

**Course Contents**

- Introduction to Ansoft and IE3D software
- Design, Simulation and Optimization of a Power Divider using IE3D software.
- Design, Simulation and Optimization of an Optimal Tee.
- Design, Simulation and Optimization of a Hybrid ring.
- Design, Simulation and Optimization of a Directional coupler.
- Fabrication and testing of Microwave Components.

**Speakers**

Prof. Shishir Jagtap  
Prof. Vandana Sawant

**Who Should Attend**

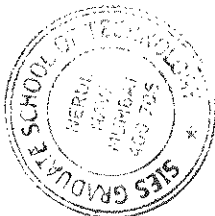
REGULAR students - First year first sem (Ses. available only for 20 students) - 10 people with 2 mentors each

**Registration Fee**

Regular Fee: FREE  
(Cost of certificate: Rs.2000/-)

**Contact for Registration**

Prof. Vandana Sawant - Room no:111. Mobile no: 9820792714 Email: [vandana.sawant@siesgst.ac.in](mailto:vandana.sawant@siesgst.ac.in)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/A, SHRI CHINAI INDUSTRIAL ESTATE, MIDC, VILASRAO  
SECTOR-4, MOPE, NAVE NEAR TITAN TOWER

Serial No.15

Event : Workshop on Design and Testing of Microwave Components using IE3D

Date: 7-9-2019

Resource Person : Prof. Shishir Jagtap Prof. Vandana Sawant, Faculty, SIESGST

Nature : Regional



SIES Graduate School of Technology  
Department of Electronics and Telecommunication Engineering  
Branch and MTTC Group  
Warananagar, Pune

**Design and Testing of Microwave Components using IE3D**

Workshop on Design and Testing of Microwave Components using IE3D, 7th to 9th September 2019, Warananagar, Pune

Workshop on Design and Testing of Microwave Components using IE3D, 7th to 9th September 2019, Warananagar, Pune. The workshop is organized by the Department of Electronics and Telecommunication Engineering, SIES Graduate School of Technology, Warananagar, Pune. The workshop is organized to provide the students with the knowledge and skills required for the design and testing of microwave components using IE3D software. The workshop is organized for the students of the Department of Electronics and Telecommunication Engineering, SIES Graduate School of Technology, Warananagar, Pune. The workshop is organized for the students of the Department of Electronics and Telecommunication Engineering, SIES Graduate School of Technology, Warananagar, Pune.

**Objectives**

- 1. To understand the basic concepts of microwave components and their design using IE3D software.
- 2. To understand the basic concepts of microwave components and their design using IE3D software.
- 3. To understand the basic concepts of microwave components and their design using IE3D software.

**Programme**

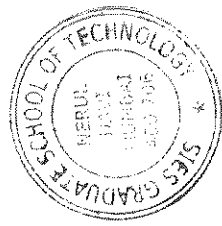
- 1. Introduction to Microwave and IE3D software.
- 2. Design of a microstrip line and its characteristics.
- 3. Design of a microstrip line and its characteristics.
- 4. Design of a microstrip line and its characteristics.
- 5. Design of a microstrip line and its characteristics.
- 6. Design of a microstrip line and its characteristics.

**Speakers**

Prof. Shishir Jagtap, Prof. Vandana Sawant

**Workshop Details**

Workshop on Design and Testing of Microwave Components using IE3D, 7th to 9th September 2019, Warananagar, Pune. The workshop is organized by the Department of Electronics and Telecommunication Engineering, SIES Graduate School of Technology, Warananagar, Pune. The workshop is organized to provide the students with the knowledge and skills required for the design and testing of microwave components using IE3D software. The workshop is organized for the students of the Department of Electronics and Telecommunication Engineering, SIES Graduate School of Technology, Warananagar, Pune.



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/A/5, SIR CHINSAJI ROAD, WARANANAGAR, PUNE  
SECTOR-4, WARANANAGAR, PUNE-411 004

Serial No.16

Event -Phoenix 1.0 IEEE Congress Participation

Date 11-9-2019

Resource person : Mr. Abhay Phansikar, Chair, IEEE Bombay Section

Nature : Regional



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



### NOTICE

06-09-2019

IEEE Bombay section is organizing a meeting and presentation competition for all IEEE SBs at KJ Somaiya Institute of Engg and IT, Slon. The interested IEEE students can register for the same. The event details are given below.

Name of the event	Phoenix 1.0
Date	11-09-2019, 10AM at KJSIEIT, Mumbai
Eligibility	Open to all IEEE Student members
Contact Person	Athulya Anand 9769011702 Sricharran 8976706250

*RG 109 rb*

*A.N. Kemkar*

Prof. Biju Balakrishnan

Dr. Atul Kemkar



*AK*

Principal  
SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



Serial No.17

Event - Robotics Workshop level1

Date 14-09-2019

Resource person Prof.Vivek Nar, Faculty, DJ Sanghvi CoE, Mumbai

Nature : Regional



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

09-09-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on Robotics for you. The interested students can register for the same. The event details are given below.

Name of the event	Robotics Workshop
Date	14-09-2019, 10 AM to 5 PM, at Lab304
Eligibility	Open to all students
Contact Person	Abhishek Singh 8425877338 Sahil Pandita 7780871070

*RG BSB*

Prof. Biju Balakrishnan

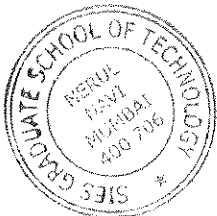
IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/E/5, SRI CHANDRASEKARENDRASARASWATHI VIDYAPURAM  
SECTOR-5, NERUL, NAVI MUMBAI - 400706.

**Serial No.18**

**Event:** The Communicator - Soft skill Enhancement Workshop

**Date:** 16-09-2019

**Resource Person:** 1. Mr. Krishna Mohan, Director, 'Craftin Images' 2.Mr. Sameer Narkar, Director, 'Konnect Insights' 3. Mr. Suyash Gogate, Marketing Head of 'Caribbean Cruise'.

**Nature:** Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



**NOTICE**

14-09-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a The Communicator - Soft skill Enhancement Workshop for you. The interested students can register for the same. The event details are given below.

Name of the event	The Communicator - Soft skill Enhancement Workshop
Date and Time	16-09-2019, 3.30 PM at Room 304
Eligibility	Open to all students
Contact Persons	Tarvi Joshi 7506486269 Simran Sharma 9987288306

*B. B. Balakrishnan*

Prof. Biju Balakrishnan

*A. N. Kemkar*

Dr. Atul Kemkar



*[Signature]*

Principal  
SIES Graduate School of Technology  
Plot 10/11, Sector 5, Nerul, Navimumbai  
Sector 5, Nerul, Navimumbai-400706

**Serial No.19**

**Event:** Seminar on higher studies abroad and other competitive Exams

**Date:** 23-09-2019

**Resource Person:** Mr. Tanmay Kudyadi and Mr.Koushik Khanra, Imperial Overseas Educational Consultant, Thane

**Nature:**Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



**NOTICE**

16-09-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a seminar on Higher Studies abroad and other competitive exams for you. The interested students can register for the same. The event details are given below.

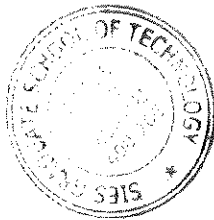
Name of the event	Seminar on Higher Studies abroad and other competitive exams
Date	23-09-2019, 3.30 PM
Eligibility	Open to all TE & BE students
Contact Persons	Tanvi Joshi 7506486269 Simran Sharma 9987288306

*RG BSRB*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/1/E, SRI CHANDRASEKENDRA SARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVI MUMBAI - 400706

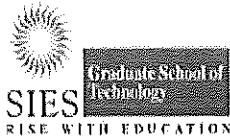
**Serial No.20**

**Event:** IEEE national Congress AISYWC19 Participation

**Date:** 28-09-2019 To 30-09-2019

**Resource Person:** Prof. Kiran Talele, Treasurer, IEEE bombay Section at CMR Group of Institutions, Hyderabad

**Nature:-**Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



**NOTICE**

06-09-2019

IEEE India Council is organizing a 3-day congress for all IEEE Student members and IEEE Young Professional at Hyderabad. The interested IEEE students can register for the same. The event details are given below.

Name of the event	AISYWC19
Date	28-09-2019 to 30-09-2019 at hyderabad
Eligibility	Open to all IEEE Student members
Contact Person	Athulya Anand 9769011702 Sricharran 8976706250

*Dg BSB*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/L/15, SRI CHANDRASEKARENDRA SARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706

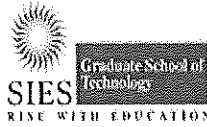
Serial No.21

Event: LaTeX Workshop

Date: 22-10-2019

Resource Person: Prof. Madhuri Kulkarni and Prof. Priyanka Kadam, Faculty, SIESGST

Nature: Regional



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

05-10-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on LATEX Report Writing. The interested students can register the said event. The event details are given below.

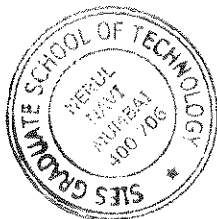
Name of the event	Workshop on LATEX Report Writing
Date and Time	22/10/2019, 10.00 am to 5.00 pm at 307 Lab
Eligibility	BE students
Contact Persons	Prof. Priyanka Kadam Prof. Madhuri Kulkarni

*RG BSRB*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*  
Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SRI CHANDRASEKARENDRASARASWATHI VIDYAPURAM  
SECTOR-5, NERUL, NAVI MUMBAI - 400 706.


**Serial No.22**


**Event** -One week STTP on Antennas and RF Components Design, Fabrication and Testing

**Date:** 16-12-2019 to 21-12-2019

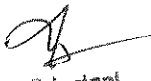
**Resource person :** Dr. Shishir Jagtap, SIESGST, Navi Mumbai Dr.Vinit Dongre, TCET, Mumbai Dr. Pramod Bhabarthe, PVPPCoE, Sion Dr.R.K.Gupta, Terna CoE, Navi Mumbai Dr.C.D.Rawat, VESIT, Mumbai Dr.Uday Pandit, SFIT, Mumbai Prof. A.S.Kurhekar, NYSSCoE, Navi Mumbai

**Nature :** Regional

<p align="center"><b>REGISTRATION FORM</b> One Week Short Term Training Course On</p> <p><b>"Antennas and RF Components Design, Fabrication and Testing"</b></p> <p align="center">December 16<sup>th</sup> to 21<sup>st</sup>, 2019.</p> <p>Name: _____</p> <p>Designation: _____</p> <p>Department: _____</p> <p>College: _____</p> <p>Address: _____</p> <p>Phone: _____</p> <p>Email: _____</p> <p>IEEE Membership Number: _____</p> <p>ISTE Membership Number: _____</p> <p>"ISTE membership is compulsory"</p> <p>Signature of Participant _____</p> <p>Signature of Principal _____</p>	<p><b>Important Dates:</b></p> <p><b>Registration Deadline:</b> 12<sup>th</sup> December 2019</p> <p><b>Course Duration:</b> 16<sup>th</sup> Dec. to 21<sup>st</sup> Dec., 2019. ( 9.30 am to 4.30 pm )</p> <p><b>Who can apply:</b></p> <p>Faculties, Research scholars, PG students from Engineering Colleges and Industry persons.</p> <p><b>Course Fee:</b></p> <p>Rs. 2000/- ( Includes certificate &amp; high tea)</p> <p>Rs. 1500/- ( For MTTs members)</p> <p><b>Speakers:</b></p> <p>Dr. Uday Pandit, Professor, SFIT, Borivali.</p> <p>Dr. C. D. Rawat, Professor, VESIT, Sion.</p> <p>Dr. R. K. Gupta, Professor, TPCE, Nerul.</p> <p>Dr. Vinit Dongre, Professor, TCET, Kandivali.</p> <p>Dr. Pramod Bhabarthe, Professor, PVPPCOE, Sion.</p> <p>Prof. A. S. Kurhekar, Professor, DMCE, Airoli.</p> <p>Prof. S. D. Jagtap, Professor, SIES GST, Nerul.</p>	 <p align="center"><b>One Week AICTE-ISTE and IEEE BS MTTs Chapter Approved</b></p> <p align="center"><b>Short Term Training Program</b> On <b>"Antennas and RF Components Design, Fabrication and Testing"</b></p> <p align="center">December 16<sup>th</sup> to 21<sup>st</sup>, 2019.</p> <p align="center"><b>Organized by</b> <b>Department of Electronics and Telecommunication Engineering and IEEE SIESGST Student Chapter</b></p> <p align="center"><b>SIES Graduate School of Technology</b> Sri Chandrasekharendra Saraswathy Vidyapuram, Plot-1 C&amp;E, Sector-5, Nerul, Navi Mumbai 400706 Phone: 02261082400/0227171719 <a href="http://www.siesgst.edu.in">www.siesgst.edu.in</a></p>
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<p><b>ABOUT SIES</b></p> <p>The South Indian Education Society (SIES) established in 1932 at Matunga is a pioneer in the field of education in this metropolis. The Society has been serving the cause of education and has carved for itself a niche as a provider of quality value based education from Nursery to Doctoral level in a wide variety of fields. The Institutional Mission by our Founders: "This Society should sincerely serve the cause of education and the educational needs of the common man of this cosmopolitan city".</p> <p><b>About SIES Graduate School of Technology</b></p> <p>SIES Graduate School of Technology, an integral part of this well-established Community, started in the year 2002, is located in the list of an educational hub in Navi Mumbai imparting quality technical education, offering four year Bachelor of Engineering courses in Electronics and Telecommunication Engineering, Computer Engineering, Information Technology, Printing &amp; Packaging Technology and Mechanical Engineering.</p> <p>The institute is approved by the All India Council for Technical Education-New Delhi. The Directorate of Technical Education Mumbai, affiliated to the University of Mumbai and accredited by the Tata Consultancy Service Limited and ISO9001:2008 certified. The institute has excellent infrastructure and state of the art fully equipped laboratories</p> <p><b>Department of Electronics &amp; Telecommunication</b></p> <p>The Department of Electronics and Telecommunication came into existence in 2002, and at present operational with an intake of 120 students. It has progressed rapidly, to be regarded, as one of the premiere departments, with excellent infrastructure. It has dedicated, highly qualified faculty members specialized in state-of-the-research areas, in order to give the students a holistic and pragmatic view of the present</p>	<p>Electronics and Communication industry.</p> <p><b>Department Vision:</b> To offer quality education in electronics and telecommunication engineering with research focus and collaborative ventures with industries to develop technocrats for socio economic growth.</p> <p><b>Department Mission:</b> (a) To provide quality education satisfying the requirements of academics, research and corporate world (b) To develop skills to cater the socio economic needs (c) To develop entrepreneurship skills.</p> <p><b>The Course Objectives</b></p> <ul style="list-style-type: none"> <li>To learn design constraints of RF components</li> <li>To gain new insights in various antenna designs and RF Design subject of semester VIII, EXTC engineering.</li> </ul> <p><b>The Course Contents</b></p> <ul style="list-style-type: none"> <li>RF Design constraints</li> <li>Design of various antennas, using IE3D Simulator</li> <li>Design of various RF components, using IE3D Simulator</li> <li>Design of Amplifier, using open source simulator.</li> <li>Reflection and Dispersion phase analysis of periodic structures.</li> <li>EMI and EMC in RF design</li> <li>RF MEMS, Fabrication and Testing of RF Components</li> </ul> <p><b>The Course Outcome</b></p> <p>Participants will be able to,</p> <ul style="list-style-type: none"> <li>To understand the design constraints of RF components.</li> <li>To design linearly and circularly polarized antennas.</li> <li>To design high gain and wideband antennas.</li> <li>To design Compact MIMO antenna.</li> <li>To design power divider, coupler, hybrid ring etc.</li> <li>To design single, dual and tri and UWB monopole antenna</li> <li>To gain new insights in periodic structures, RF MEMS and RF Design Sem. 8 subject, of EXTC Engineering.</li> <li>To design, fabricate and test RF components.</li> </ul>	<p><b>PATRONS</b></p> <p>Dr. V. Shankar Hon. President, SIES.</p> <p>Shri. M. V. Ramnarayan Hon. Secretary, SIES.</p> <p>Dr. P. V. Parmeswaran Hon. Advisor, SIESGST.</p> <p><b>CONVENOR</b></p> <p>Dr. A. N. Kemkar, Principal (I/C), SIESGST.</p> <p>Dr. Preeti Hemnani, Professor, SIESGST.</p> <p><b>ORGANIZING COMMITTEE</b></p> <p><b>Program Coordinators</b></p> <p>Prof. Shishir Jagtap Mobile :+918097810351</p> <p>Prof. Biju Balakrishnan Mobile: 9820408540</p> <p><b>Organizing Members</b></p> <p>Prof. Vandana Sawant Mobile: +919820755314</p> <p>Prof. Hema Raut Mobile: +919969004169</p> <p><b>REGISTER HERE</b> (Scan QR code for registration link or <a href="#">click</a>)</p> 
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**Principal**  
**S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY**  
 PLOT 1C/D/E, SRI CHANDRASEKHAR SARA SWATHY VIDYAPURAM  
 SECTOR-5, NERUL, NAVI MUMBAI - 400706.

**Serial No.23**

**Event:**Digital Design Hacks 101: Way to Digital Craftsmanship (Webinar)

**Date:**18-06-2020 To 19-06-2020

**Resource Person:** Technical Team of IEEE SIESGST Student Branch

**Nature:** Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbal-400706



### NOTICE

12-06-2020

IEEE SIESGST Student Branch and Department of EXTC are organizing a Webinar on Digital craftsmanship for you. The interested students can register for the same. The event details are given below.

Name of the event	Digital Design Hacks 101
Date	18-06-2020 & 19-06-2020, 10 AM to 5 PM
Eligibility	Open to all students
Contact Person	Atharva Karnik 7738410588 Sricharran 8976706250

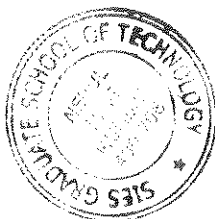
Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

Dr. Preeti Hemnani  
HoD, Dept. of EXTC

**Year 2018-19**

**Serial No.16**

**Event:** FPGA design using VHDL



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PLOT 10/1/5, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAL-400706

Date: 4-8-2018,5-8-2018 and 29-9-2018

Resource Person: Preeti H,Pranavi M,Sonal J,Abhishek K

Nature: Regional



**SIES Graduate School of Technology**  
Department of Electronics and Telecommunication

Presents  
**3 days Hands on Training on  
FPGA Design using VHDL**  
August 4<sup>th</sup>,5<sup>th</sup> & September 29<sup>th</sup>,2018 9.30 AM to 4.00 PM

Since their introduction in the 1980s, field programmable gate arrays (FPGAs) have become increasingly important to the electronics industry. They have the potential for higher performance and lower power consumption than microprocessors and compared with ASICs, offer lower non-recurring engineering (NRE) costs, reduced development time, easier debugging and reduced risk. Since modern FPGAs can meet many of the performance requirements of ASICs, they are being increasingly used in their place. The aim of workshop is to provide a platform for students to learn, design and implement digital systems on FPGA using VHDL.

**Objectives**

Introduction to VLSI design flow & concepts of FPGA's & CPLD's  
Implementation of combinational & sequential circuits on FPGA using VHDL.  
Design of Finite State Machine & its implementation on FPGA using VHDL.

**Course Contents**

Introduction to VLSI design flow & concepts of FPGA's & CPLD's  
Combinational circuit design using VHDL & its FPGA implementation.  
Sequential circuit design using VHDL & its FPGA implementation.  
Design of Finite State Machine (FSM) using VHDL & its FPGA implementation.

**Speakers**

Prof. Preeti Hemant  
Prof. Abhishek Kadam  
Prof. Pranavi Mishra

**Who Should Attend**

Any student from SIES (INSTITUTE) - First course first serve (Seat available only for 20 students- 10 groups with 2 members each)

**Registration Fees**

Course Fees: FREE  
(Cost of certificate will be applicable)

**Contact for Registration**

Prof. Pranavi Mishra  
Email ID : pranavi.mishra@siesgst.ac.in

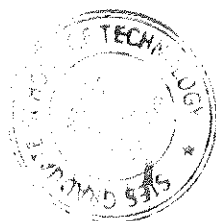
Serial No.17

Event: ARM Mbed and Node MCU workshop

Date: 19-12-2018 - 21-12-2018

Resource Person: Pushkar Sathe, Hema Raut

Nature: Regional



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SIES GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/105, SION ROAD  
SECTOR 10, MUMBAI 400 022





## SIEST Graduate School of Technology

Department of Electronics and Telecommunication

Presents

5 days Hands on Training on

Embedded Systems - ARM Mbed Platform and Node MCU

19th-21st December 2018, 9.30 AM to 4.00 PM

And remaining 2 days in January 2019.

There is difference between education and knowledge. Education provides learning. While knowledge transfers that learning into a career that sustains a living. But the truth is, our education system is largely unrooted around academic learning, leaving the task of turning it into a career to the individual. For the less-privileged though, the only barrier that stands between them and a technocrat is knowledge of practical aspects of technology.

This course is meant to be a hands-on type course, giving students a chance to hear and read about embedded system topics, and then put those concepts to work by developing and debugging embedded system hardware and firmware.

### Objectives

- i) Learn about Embedded systems and ARM Mbed platform
- ii) Interfacing various devices like 7 segment display, LCD display.
- iii) Interfacing various sensors like accelerometer, LDR, temperature sensor etc.
- iv) Learn basics of Node MCU and its interfacing with sensors and IOT application designing.

### Course Contents

Module 1: Introduction to microprocessor and microcontroller, Introduction to Embedded systems, Introduction to ARM Mbed platform.

Module 2: Simple programs like LED Blinking, use of I/O and Interrupt function.

Module 3: Basics of 7 segment display and LCD and their interfacing with freedom board.

Module 4: Basics of accelerometer, LDR, piezoelectric and its interfacing with freedom board.

Module 5: Basics of Node MCU, its interfacing with various sensors, Designing of IOT applications based on Node MCU.

### Who Should Attend

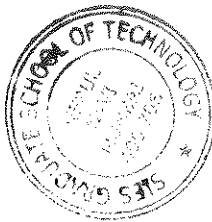
Any student from SIEST (ELECTRONICS) - First course first semester (Seat available only for 20 students - 10 groups with 2 members each)

### Registration Fees

Course Fees: 100/- Rs per member  
(to be paid in the office)

### Contact for Registration

Prof. Poojankar Sathu  
Mobile: 9370630637  
Email ID: psojankar.sathu@siestgslac.in



Principal

SIEST, GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/1/5, STREET NO. 10, NEAR CHITRAVATI VILLAGURAM  
SECTOR 4, PUNE 411 004. TEL: 020-26076000

Serial No.18

Event: Embedded systems and IoT

Date: 19/01/2019

Resource Person: Mr.Vishal Gaikwad and Prof.Abhishek Kadam

Nature: Regional



**SIES Graduate School of Technology**  
Department of Electronics and Telecommunications  
**Presents**

One day Hands on Training on "Workshop on Arduino with sensor and display interfacing."  
19<sup>th</sup> January 2019, Navi Mumbai, 09.00 am to 05.00 pm

**Objectives**

- Introduction to Microcontroller and difference between Microprocessor and Microcontroller
- Introduction to Arduino Board.
- To know the importance of different peripheral devices and their interfacing to microcontrollers.
- To write C/C++ language programs for Arduino board
- To know the sensor interfacing with Arduino and its programming.
- To know the display interfacing with Arduino and its programming.

**Course Contents**

- Arduino Board and its programming via Arduino IDE
- Working principle and interfacing of.
- Sensors (LDR, Ultrasonic, Gas Sensor, Accelerometer)
- Three display devices (Seven Segment, 16X2 LCD, LED)
- Project Designing using Arduino Board, Sensor and Display Devices

**Who Should Attend**

- Any student from SE (EXTC/CENT/MECH/COMP)

**Registration Fees**

Free

**Contact for Registration**

Prof. Vishal Gaikwad, Mob. - 966579359  
Email ID: gaikwad.vishal@siesgst.ac.in

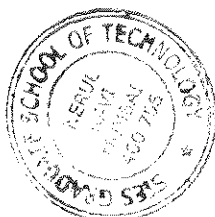
Serial No.19

Event: Embedded system: ARM Mbed and Node MCU

Date: 24-6-2019 - 28-6-2019

Resource Person: Mr.Pushkar Sathe,Ms.Vaishali Mangrulkar,Ms.Pranjali Thakre

Nature: Regional



**SIES**  
**SIES, GRADUATE SCHOOL OF TECHNOLOGY**  
SIES/IC/D/E, SIES GRADUATE SCHOOL OF TECHNOLOGY, NAVI MUMBAI, VILVAPOURAM  
SECTOR 4, MIDC, NAVI MUMBAI - 401 306.

**SIES Graduate School of Technology**  
Department of Electronics and Telecommunication  
Presents  
**5 days Hands on Training on**  
**Embedded Systems - ARM Mbed Platform and Node MCU**

**24th-28th June 2019, 9.30 AM to 4.00 PM**

There is difference between education and knowledge. Education provides learning. While knowledge translates that learning into a career that earns a living. But the truth is, our education system is largely structured around academic learning, leaving the task of turning it into a career to the individual. For the less-privileged though, the only barrier that stands between them and a tech-career is knowledge of practical aspects of technology. This course is meant to be a hands-on type course, giving students a chance to hear and read about embedded system topics, and then put those concepts to work by developing and debugging embedded system hardware and firmware.

**Objectives**

- Learn about Embedded systems and ARM Mbed platform
- Interfacing various devices like 7 segment display, LCD display.
- Interfacing various sensors like accelerometer, LDR, temperature sensor etc.
- Learn basics of Node MCU and its interfacing with sensors and
- Learn IOT application designing using Node MCU.

**Course Contents**

**Module 1:** Introduction to microprocessor and microcontroller, Introduction to Embedded systems, Introduction to ARM Mbed platform.

**Module 2:** Simple programs like LED Blinking, use of PinOut and PinOut function.

**Module 3:** Basics of 7 segment display and LCD and their interfacing with freeduino board.

**Module 4:** Basics of accelerometer, LDR, potentiometer and its interfacing with freeduino board.

**Module 5:** Basics of Node MCU, its interfacing with various sensors, Designing of IOT applications based on Node MCU.

**Who Should Attend**

Any student from SE (ECE/ETCE)- Para course first year (Seat available only for 20 students- 10 groups with 2 members each)

**Registration Fees and Faculty**

Course Fees: FREE

(Cost of certificate will be applicable)

Faculty : Pushkar S., Vaishali K., Prajash T.

**Contact for Registration**

Prof. Pushkar S.  
Mobile: 9870630637

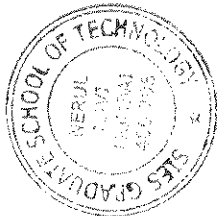
**Serial No.20**

**Event :Workshop on Advanced Antenna Design using IE3D**

**Date : 04-07-2018 11-07-2018 and 18-07-2018**

**Resource Person : Shishir Jagtap, Vandana Sawant**

**Nature :Regional**



  
Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIES CAMPUS, VIKHARWADI, VIKHARWADI, VIJAYAPURAM  
SECTOR-4, NERUL, MUMBAI - 401 305

**SIES Graduate School of Technology**  
Department of Electronic and Telecommunication Engineering  
Presents

3 days Hands on Training on

**Advanced Antenna Design using IE3D**

July 04<sup>th</sup>, July 11<sup>th</sup> and 18<sup>th</sup> July of 2018 Timing : 10.00 AM to 4.00 PM, Venue : Lab 316

Since the invention of the Microstrip Antenna four decades ago, the demand for the applications has been increasing rapidly, especially within the last two decades. However, those applications have been in demand mainly by the Department of Defense, because of their extremely thin profile (0.001 to 0.025 wavelength), printed Microstrip Antennas have found many applications in millimetre wave, wireless, radars, and satellites. The Microstrip Antenna, because of low shield loss, high voltage, low profile, and low manufacturing cost, is finding increasing applications in the commercial sector of the industry. This workshop aims at discussing latest trends in the antenna structural design software and present several programs associated with the antenna commercial applications, such as: mobile satellite systems, direct broadcast satellite systems, global positioning systems, medical hyperthermia therapy, etc.

**Objectives**

- Introduction to basic understanding and designing of the Patch Antenna.
- Simulation of the Patch Antenna using structural software IE3D.
- To analyze, design and simulate different types of patch antennas suitable for numerous applications like microwave communication, radar, mobile communication, RFID, SCOT applications etc.

**Course Contents**

- Introduction to Antenna and IE3D software
- Design, Simulation and Optimization of a Microstrip Patch Antenna using IE3D software
- Design, Simulation and Optimization of a Microstrip Patch Antenna Array.
- Design, Simulation and Optimization of a Microstrip Patch MIMO Antenna.
- Parameter enhancement of MSA by designing different types of patch structure.
- Design of Whisker Component, Fractal Antenna, Reconfigurable Antenna and Fabrication of antenna.

**Speakers**

Prof. Mahesh Chougale  
Prof. Sandeep Gokhale

**Who Should Attend**

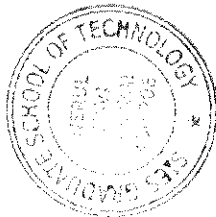
UG and PG, ECE, EEE, IT students - First come first serve basis (only for 50 students) - 10 groups with 5 members each

**Registration Fees**

₹ 20000 (Incl. of material and transportation)

**Contact for Registration**

Prof. Sandeep Gokhale - Room no: 311 - Mobile no: 9820755211-432641 - [sandeepgokhale@siesgst.edu.in](mailto:sandeepgokhale@siesgst.edu.in)



*(Signature)*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/16, SIES VIDYAVIHAR, VIDYAPURAM  
SECTOR 4, NEEMCHANDRA, CHENNAI - 600 074

Serial No.21

Event: Circuit Maniacs

Date: 06-09-2018

Resource person "Tech Team, IEEE SIESGST student branch

Nature : National



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbal-400706



## NOTICE

27-08-2018

IEEE SIESGST Student Branch and Department of EXTC are organizing a mini project competition-Circuit Maniacs or you. The interested students can register the said event. The event details are given below.

Name of the event	Circuit Maniacs
Date and Time	06-09-2018, 10 AM
Eligibility	Open to all students
Contact Persons	Athulya 9769011702 Simran 9987288306

*RG BSB*

Prof. Biju Balakrishnan

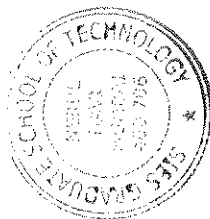
IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HOD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/11/5, S.I.T  
SECTOR-5, NERUL, NAVIMUMBAL-400706, MUMBAI

**Serial No.22**

**Event:** Seminar on Smart Manufacturing and Blockchain at IIT Bombay

**Date:** 26-09-2018

**Resource person** "IIT Bombay

**Nature :** National



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



### NOTICE

20-09-2018

IIT Bombay is organizing a Seminar on Smart Manufacturing and Block chain at IITB. The interested students can register the said event. The event details are given below.

Name of the event	Seminar on Smart Manufacturing and Blockchain at IIT Bombay
Date and Time	26-09-2018, 10 AM at IIT Bombay
Eligibility	IEEE core and tech team members
Contact Persons	Biju Balakrishnan 9820408540

*Biju Balakrishnan*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HOD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/3/3, SECTOR 5, NERUL, NAVIMUMBAI-400706  
SECTORAL OFFICE

Serial No.23

Event:IEEE Day celebration and Seminar on Mobile App development

Date 10-10-2018

Resource person "Tech Team, IEEE SIESGST student branch

Nature : National



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

03-10-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing an event: IEEE Day celebration and Seminar on Mobile App development for you. The interested students can attend the said event. The event details are given below.

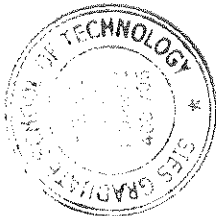
Name of the event	IEEE Day celebration and Seminar on Mobile App development
Date and Time	10-10-2018, 3.30 PM at GST Audi
Eligibility	Open to all students
Contact Persons	Athulya 9769011702 Simran 9987288306

*RS BSB*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N.Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*

Principal  
SIES GRADUATE SCHOOL OF TECHNOLOGY  
PLOT NO.15, SECTOR 5, NERUL, NAVIMUMBAI-400706

Serial No.26

Event : C Programming workshop

Date:14/1/2019

Resource person "Tech Team, IEEE SIESGST student branch

Nature : Regional



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Sector 5, Nerul, Navimumbai-400706



### NOTICE

07-01-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on C Programming for you. The interested students can register the said event. The event details are given below.

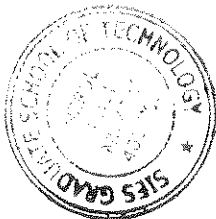
Name of the event	Workshop on C Programming
Date and Time	14-01-2019, 3.30 PM at Room 304
Eligibility	Open to all students
Contact Persons	Athulya 9769011702 Simran 9967288306

*RS BSB*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HOD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/01/3, SIES COLONY, CHANDRASEKHAR NAGAR,  
SECTOR-5, NERUL, NAVIMUMBAI-400706



**Serial No.27**

**Event :** Seminar on Higher Studies

**Date:** 15-01-2019

**Resource person :** Prof. Koushik K,Imperial Classes

**Nature:**Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



**NOTICE**

10-01-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Seminar on Higher Studies for you. The interested students can register the said event. The event details are given below.

Name of the event	Seminar on Higher Studies
Date and Time	15-01-2019, 3.30 PM at Room 401
Eligibility	Open to all TE EXTC students
Contact Person	Abhishek Singh 8425877338

*RS BSB*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC

**Serial No.28**

**Event :** IEEE Bombay section Congress, Nashik

**Date:** 18-01-2019 - 20-01-2019



*[Signature]*

Principal  
SIES Graduate School of Technology  
Plot 10/105, Sector 5, Nerul,  
Mumbai - 400706

Principal  
SIES Graduate School of Technology

Resource person : IEEE Bombay section

Nature:National



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

07-01-2019

IEEE Bombay Section is organizing an event: IEEE Bombay section Congress, Nashik for you. The interested IEEE members can register the said event. The event details are given below.

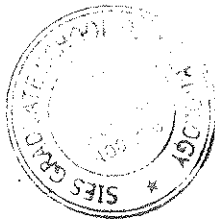
Name of the event	IEEE Bombay section Congress, Nashik
Date and Time	18-01-2019 to 20-01-2019 at Sandip University Campus, Nashik
Eligibility	Open to all IEEE students
Contact Persons	Biju Balakrishnan 9820408540

*Biju Balakrishnan*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/5, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR 5, NAVIMUMBAI, MUMBAI - 400706

Serial No.29

Event : Arduino Workshop

Date: 19-01-2019

Resource person : Prof.Vishal Gaikwad,Prof.Abhishek Kadam,

Nature:Regional



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Sector 5, Nerul, Navimumbai-400706



## NOTICE

16-01-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing an Arduino Workshop for you. The interested students can register for the same. The event details are given below.

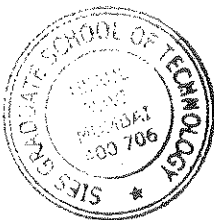
Name of the event	Arduino Workshop
Date and Time	19-01-2019, 9.30 AM to 4.30 PM at CC
Eligibility	Open to all students
Contact Persons	Nivedita Deshpande 7506656431

*Dr BSB*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N.Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*

Principal  
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SECTOR 5, NERUL, NAVIMUMBAI-400706

Serial No.30

Event: Latex Software Workshop

Date: 20-02-2019

Resource Person: Prof. Madhuri Kulkarni , Prof.Priyanka Kadam

Nature: Regional



**SIES Graduate School of Technology**  
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Sector 3, Nerul, Navimumbai-400706

## NOTICE

05-02-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on LATEX Report Writing. The interested students can register the said event. The event details are given below.

Name of the event	Workshop on LATEX Report Writing
Date and Time	20/02/2019, 10.00 am to 5.00 pm at Room 118
Eligibility	BE students
Contact Persons	Prof. Priyanka Kadam Prof. Madhuri Kulkarni

*DG BSRB*

Prof. Biju Balakrishnan

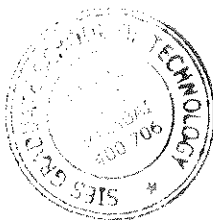
IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-3, NERUL, NAVIMUMBAI-400706

Serial No.30

Event : Workshop on Motor driver

Date: 21-01-2019

Resource person : IEEE technical team

Nature:Regional



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Sector 5, Nerul, Navimumbai-400706



## NOTICE

14-01-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on Motor driver for you. The interested students can register the said event. The event details are given below.

Name of the event	Workshop on Motor driver
Date and Time	21-01-2019, 3.30 PM at Room 401
Eligibility	Open to all students
Contact Persons	Athulya 9769011702 Simran 9987288306

*B. B. Balakrishnan*

Prof. Biju Balakrishnan

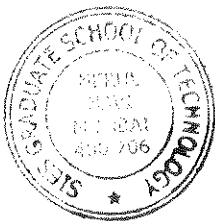
IEEE SB Branch Counselor

& Event coordinator

*A. N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*A. N. Kemkar*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT NO/D/E, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI - 400706

Serial No.31

Event : Seminar on placement preparation

Date: 22-01-2019

Resource person : Ms. Pooja Vijayakumaran

Nature:Regional



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

15-01-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Seminar on placement preparation for you. The interested students can attend the said event. The event details are given below.

Name of the event	Seminar on placement preparation
Date and Time	22-01-2019, 3.30 PM at Room 401
Eligibility	Open to all TE EXTC students
Contact Persons	Athulya 9769011702 Simran 9987288306

*Rg Bg B*

Prof. Biju Balakrishnan

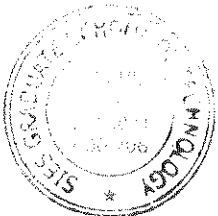
IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI - 400 706.

Serial No.32

Event : Workshop on self skill assay

Date: 28-01-2019

Resource person : Prof. Kavita Patil, Sandip Institute and Research Center, Nashik

Nature:Regional



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

24-01-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on self skill assay for you. This is mandatory for all TE EXTC students. The event details are given below.

Name of the event	Workshop on self skill assay
Date and Time	28-01-2019, 9 AM to 11AM SIESCoM Audi
Eligibility	Open to all TE EXTC students
Contact Persons	Nivedita Deshpande 7506656431

*RS 109 tb*

Prof. Biju Balakrishnan

IEEE SB Branch Counselor

& Event coordinator

*A.N.Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/5, SRI CHANDRASEKENDRA SARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI - 400706

**Serial No.33**

**Event :** Research to product and patent

**Date:** 28-01-2019

**Resource person :** Dr. Gayatri Phade, Sandip Institute and Research Center, Nashik

**Nature:**Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyalayam  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

24-01-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Seminar on Research to product and patent for you. It is mandatory for all EXTC third year students. The event details are given below.

Name of the event	Seminar and case study presentation on Research to product and patent
Date and Time	28-01-2019, 11.30 to 1.30 Pm at SIESCoM Audi
Eligibility	Open to all TE EXTC students
Contact Persons	Abhishek Singh 8425877338

*Dr. Biju Balakrishnan*

Prof. Biju Balakrishnan

IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/015, SECTOR 5, NAVIMUMBAI, MUMBAI-400706  
SECTOR 5, NERUL, NAVIMUMBAI, MUMBAI-400706



Serial No.34

Event : Arduino Workshop

Date: 16-02-2019

Resource person : Dr. Saurabh Mehta and Mr.Hannan satopay (VIT,Wadala)

Nature:Regional



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Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 3, Nerul, Navimumbai-400706



## NOTICE

11-02-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing an Arduino Workshop for you. The interested students can register for the same. The event details are given below.

Name of the event	Arduino Workshop
Date and Time	16-02-2019, 9.30 AM to 4.30 PM at CC
Eligibility	Open to all students
Contact Persons	Nivedita Deshpande 7506656431

*Biju Balakrishnan*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/01E, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR 3, NERUL, NAVI MUMBAI - 400706.

Serial No.35

Event: Latex Software Workshop

Date::20/2/2019

Resource person :Prof. Priyanka Kadam,Prof. Madhuri Kulkarni

Nature:Regional



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Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706

## NOTICE

05-02-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on LATEX Report Writing. The interested students can register the said event. The event details are given below.

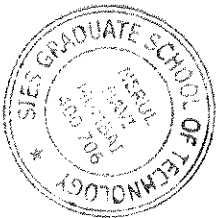
Name of the event	Workshop on LATEX Report Writing
Date and Time	20/02/2019, 10.00 am to 5.00 pm at Room 118
Eligibility	BE students
Contact Persons	Prof. Priyanka Kadam Prof. Madhuri Kulkarni

*Dr Biju*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 107/15, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706

**Serial No.36**

**Event :** Seminar on IEEE Xplore Digital Library

**Date:** 25-02-2019

**Resource person :** Dr Dhanu Pattanashetti (IEEE University Partnership Program Manager)

**Nature:**Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



**NOTICE**

18-02-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Seminar on IEEE Xplore Digital Library for you. The interested students and faculty can register for the same. The event details are given below.

Name of the event	Seminar on IEEE Xplore Digital Library
Date and Time	25-02-2019, 3.30 PM at GST Audi
Eligibility	Open to all students and faculty
Contact Person	Biju Balakrishnan 9820408540

*Biju Balakrishnan*

Prof. Biju Balakrishnan

IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC

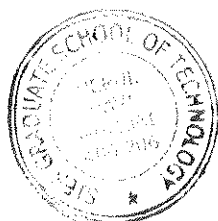
**Serial No.37**

**Event :** Technical Talk and Interaction: Design and Development of Analog Instruments

**Date:** 08-03-2019

**Resource person :** Mr. Arun P Wagale

**Nature:**Regional



*Arun P Wagale*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/15, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

06-03-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Technical Talk and Interaction: Design and Development of Analog Instruments for faculty and IEEE Tech Team. The interested Tech team members can register for the same. The event details are given below.

Name of the event	Technical Talk and Interaction: Design and Development of Analog Instruments by A P Wagle
Date and Time	08-03-2019, 3.30 PM at lab 306
Eligibility	Open to all faculty and IEEE Tech team members
Contact Person	Biju Balakrishnan 9820408540

*Biju Balakrishnan*

Prof. Biju Balakrishnan

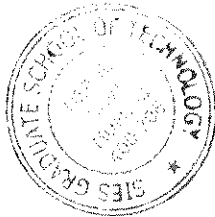
IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/5, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-V, NERUL, NAVIMUMBAI-400706

Serial No.38

Event : FPGA Workshop

Date:16-03-2019

Resource person : Dr. Satyanarayana, TIFR

Nature:Regional



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

08-03-2018

IEEE SIESGST Student Branch and Department of EXTC are organizing a FPGA Workshop for you. The interested students can register for the same. The event details are given below.

Name of the event	FPGA Workshop by TIFR Scientists
Date and Time	16-03-2019, 9AM to 5 PM at CC
Eligibility	Open to all students
Contact Persons	Rashmita Shetty 9167106508

*RG BSB*

Prof. Biju Balakrishnan

IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 30/1/5, SECTOR 5, NERUL, NAVIMUMBAI-400706  
SECTOR-5, NERUL, NAVIMUMBAI-400706

Serial No.39

Event : Seminar On Python

Date:18-03-2019

Resource person : Technical committee of IEEE SIES GST

Nature:Regional



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Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

11-03-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Seminar on Python for you. The interested students can register for the same. The event details are given below.

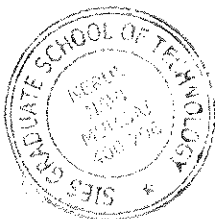
Name of the event	Seminar On Python
Date and Time	18-03-2019, 3.30 PM at Lab 305
Eligibility	Open to all students
Contact Persons	Sricharran 8976706250

*Bg 109/16*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N.Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/115, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706

Serial No.40

Event : Machine Learning Workshop

Date:23-03-2019

Resource person : Prof. Santhosh Chapaneri

Nature:Regional



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Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

18-03-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Machine Learning Workshop for you. The interested students can register for the same. The event details are given below.

Name of the event	Machine Learning Workshop by IEEE Bombay Section
Date and Time	23-03-2019, 9am to 4 PM at CC
Eligibility	Open to all students
Contact Persons	Nivedita Deshpande 7506656431

*Dr Biju*

Prof. Biju Balakrishnan

IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/6, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706

Serial No.41

Event : PCB Making workshop

Date:30/3/2019

Resource person :Prof. Prashant Ahire and Prof.Madhuri Kulkarni, SIESGST, Navi Mumbai

Nature : Regional



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Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400705



### NOTICE

25-03-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on PCB Design and fabrication. The interested students can register the said event. The event details are given below.

Name of the event	Workshop on PCB Design and fabrication
Date and Time	30/03/2019, 10.00 am to 5.00 pm at Room 401, PCB lab
Eligibility	SE students
Contact Persons	Prof. Prashant Ahire, Prof. Madhuri Kulkarni

*Dr. Balu*

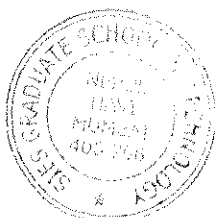
Prof. Balu Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HOD, Dept. of EXTC

Year 2017-18

Serial No.7



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 100/5, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR 5, NERUL, NAVIMUMBAI - 400705



**Event :** Embedded systems- ARM Mbed workshop

**Date:** 20-12-2017 to 22-12-2017

**Resource Person:** Pushkar Sathe

**Nature :** Regional



**SIES Graduate School of Technology**  
**Department of Electronics and Telecommunication**

Presents  
**3 days Hands on Training on**  
**Embedded Systems - ARM Mbed Platform**

**20th-22nd December 2017, 9.30 AM to 4.00 PM**

There is difference between education and knowledge. Education provides learning. While knowledge translates that learning into a career that earns a living. But the truth is, our education system is largely structured around academic learning, leaving the task of turning it into a career to the individual. For the less-privileged though, the only barrier that stands between them and a technical is knowledge of practical aspects of technology. This course is meant to be a hands-on type course, giving students a chance to hear and read about embedded system topics, and then put these concepts to work by developing and debugging embedded system hardware and firmware.

**Objectives**

- i) Learn about Embedded systems and ARM Mbed platform
- ii) Interfacing various devices like 7 segment display, LCD display.
- iii) Interfacing various sensors like accelerometer, LDR, temperature sensor etc.

**Course Contents**

**Module 1:** Introduction to microprocessor and microcontroller, Introduction to Embedded systems, Introduction to ARM Mbed platform.

**Module 2:** Simple programs like LED Blinking, use of PushOut and PassOut function.

**Module 3:** Basics of 7 segment display and LCD and their interfacing with freedom board.

**Module 4 :** Basics of accelerometer, LDR, potentiometer and its interfacing with freedom board.

**Who Should Attend**

Any student from TE (ENTC/TACE) - First year that serve (Seat available only for 20 students- 10 groups with 2 members each)

**Registration Fees**

Course Fees: FREE  
(Cost of certificate will be applicable)

**Contact for Registration**

Prof. Pushkar Sathe  
Mobile: 9870630637  
Email ID :pushkar.sathe@siesga.ac.in

**Serial No.8**

**Event:** FPGA design using VHDL

**Date:** 20-12-2017 To 22-12-2017

**Resource Person:** Prof. Preeti Hemnani ,Prof. Pranavi Mhatre ,Prof. Sonal Hutke

**Nature:** Regional



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/S, SDR CHENNAI ROAD, SIES CAMPUS, JAMSHEDPUR  
SECTOR-04, BSAUL, NAYAGUDA - 751005.



**SIES Graduate School of Technology**  
Department of Electronics and Telecommunication Engineering  
Presents

4 days Hands on Training on

**FPGA Design using VHDL**

December 20-22, 2017, 9.30 AM to 4.00 PM

Since their introduction in the 1980s, field programmable gate arrays (FPGAs) have become increasingly important to the electronics industry. They have the potential for higher performance and lower power consumption than microprocessors and compared with ASICs, offer lower non-recurrent engineering (NRE) costs, reduced development time, easier debugging and reduced risk. Since modern FPGAs can meet many of the performance requirements of ASICs, they are being increasingly used in their place. The aim of workshop is to provide a platform for students to learn, design and implement digital system on FPGA using VHDL.

**Objectives**

Introduction to VLSI design flow & concepts of FPGA's & CPLD's implementation of combinational & sequential circuits as FPGA using VHDL. Design of Finite State Machine & its implementation on FPGA using VHDL.

**Course Contents**

Introduction to VLSI design flow & concepts of FPGA's & CPLD's. Combinational circuit design using VHDL & its FPGA implementation. Sequential circuit design using VHDL & its FPGA implementation. Design of Finite State Machine (FSM) using VHDL & its FPGA implementation.

**Speakers**

Prof. Pratik Harshani  
Prof. Praveen Bhogawar  
Prof. Sumanth Kumar  
Prof. Priyanka Kadam

**Who Should Attend**

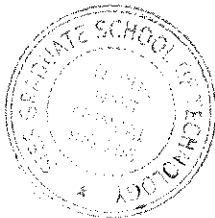
*Any student from SE/TE - This course has scope (Seat available only for 20 students- 10 groups with 2 members each)*

**Registration Fees**

Course Fee: FREE  
(Cost of certificate will be applicable)

**Contact for Registration**

Prof. Sumanth Kumar  
Email ID: ssuman.jatkar@siesgpw.ac.in



Principal  
S.I.E.S. Graduate School of Technology  
PLOT 100/5, SECTOR-7, MIDC, WADI, PUNE-411 004  
SECTION-4, MARULI, WADI, PUNE-411 004

## Serial No.9

**Event:-** Embedded Systems Design using Arduino and Introduction to IOT

**Date:** 26-12-17 To 30-12-2017

**Resource Person:** Vishal Gaikwad

**Nature :** Regional



### SIES Graduate School of Technology

Department of Electronics and Telecommunications

#### Presents

Five days Hands on Training on "Embedded System Design using Arduino"  
26th to 30th December 2017, Navi Mumbai, 10.00 am to 4.30 pm

#### Objectives

- To develop the background knowledge and core expertise of an embedded system design.
- Introduction to Arduino Board.
- To know the importance of different peripheral devices and their interfacing to microcontroller
- To know the sensor interfacing and its programming.
- To write C/C++ language programs for Arduino board for various applications.
- To know the working of different sensors and their use in an embedded systems

#### Course Contents

- Arduino Board and its programming via Arduino IDE
- Working principle and interfacing of,
- Seven sensors (LDR, Ultrasonic, Gas Sensor, Proximity Sensor, Temperature, Humidity, Accelerometer)
- Two wireless modules for data transmission (Bluetooth, Wifi module)
- Two actuators (Servo Motor, DC Motor)
- Three display devices (Seven Segment, 16X2 LCD, LED)
- Concept of Internet of Things(IoT)
- Project Designing using Arduino Board, Sensor and Display Devices

#### Who Should Attend

- Any student from SE (EXTC/CBE/IT/MECH/COMP) – First Come First Serve (Seat available for 20 students- 10 groups with 2 members each)

#### Registration Fees

Course Fees: Free

#### Contact for Registration

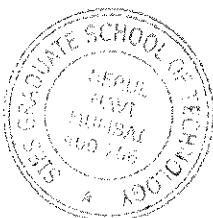
[Click Here](#)

Prof. Vishal Gaikwad, Mob. - 9665779359  
Email ID: gaikwad.vishal@siesgst.ac.in

## Serial No.10

**Event:** Arduino based Embedded system design & introduction to IOT

**Date:** 20-6-2018 To 23-6-2018



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT NO.10/15, SECTOR-10, NAVI MUMBAI  
SECTIONAL ENGINEER, NAVI MUMBAI

Resource Person: Prof. Vishal Gaikwad

Nature: Regional



### SIES Graduate School of Technology

Department of Electronics and Telecommunications

#### Presents

Five days Hands on Training on "Embedded System Design using Arduino"  
20th to 23<sup>rd</sup> June 2018, Navi Mumbai, 10.00 am to 4.30 pm

#### Objectives

- To develop the background knowledge and core expertise of an embedded system design.
- Introduction to Arduino Board.
- To know the importance of different peripheral devices and their interfacing to microcontrollers.
- To know the sensor interfacing and its programming.
- To write C/C++ language programs for Arduino board for various applications.
- To know the working of different sensors and their use in an embedded systems.

#### Course Contents

- Arduino Board and its programming via Arduino IDE
- Working principle and interfacing of,
- Seven sensors (LDR, Ultrasonic, Gas Sensor, Proximity Sensor, Temperature, Humidity, Accelerometer)
- Two wireless modules for data transmission (Bluetooth, WiFi module)
- Two actuators (Servo Motor, DC Motor)
- Three display devices (Seven Segment, 16X2 LCD, LED)
- Concept of Internet of Things(IoT)
- Project Designing using Arduino Board, Sensor and Display Devices

#### Who Should Attend

- Any student from SE (ENTC/CE/IT/MECH/COMP) – First Come First Serve (Seat available only for 20 students- 10 groups with 2 members each)

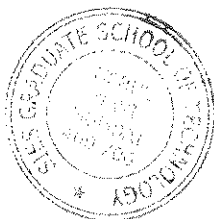
#### Registration Fees

Course Fees: Free

#### Contact for Registration

[Click Here](#)

Prof. Vishal Gaikwad, Mob. - 9665779339  
Email ID: gaikwad.vishal@siesgst.ac.in



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/113  
SECTOR-42, MIDC, NAVI MUMBAI

**Serial No.11**

**Event:** Programming using C & C++

**Date:** 26-12-2017 - 28-12-2017

**Resource Person:** Prof. Vivek Ramakrishnan

**Nature :** Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, NavMumbai-400706



## NOTICE

18-12-2017

IEEE SIESGST Student Branch and Department of EXTC are organizing a workshop on C Programming for you. The interested students can register the said event. The event details are given below.

Name of the event	Programming using C & C++ workshop
Date and Time	26-12-2017 to 28-12-2017, 10 AM to 4 PM
Eligibility	Open to all SE EXTC students
Contact Persons	Abhishek Singh 8425877338

*RG BSB*

Prof. Biju Balakrishnan

IEEE SB Branch Counselor

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/1, SECTOR 5, NERUL, NAVMUMBAI-400706

Serial No.12

Event:- Embedded systems- ARM Mbed workshop

Date: 19-6-2018 To 21-6-2018

Resource Person: Pushkar Sathe , Hema Raut

Nature : Regional



### SIES Graduate School of Technology

Department of Electronics and Telecommunication

Presents

3 days Hands on Training on  
Embedded Systems - ARM Mbed Platform

19<sup>th</sup>, 20<sup>th</sup> and 21<sup>st</sup> June 2018, 9.30 AM to 4.00 PM

There is difference between education and knowledge. Education provides learning. While knowledge translates that learning into a career that earns a living. But the truth is, our education system is largely structured around academic learning, leaving the task of turning it into a career to the individual. For the less-privileged though, the only barrier that stands between them and a technological knowledge of practical aspects of technology. This course is meant to be a hands-on type course, giving students a chance to hear and read about embedded system topics, and then put these concepts to work by developing and debugging embedded system hardware and firmware.

#### Objectives

- i) Learn about Embedded systems and ARM Mbed platform
- ii) Interfacing various devices like 7 segment display, LCD display.
- iii) Interfacing various sensors like accelerometer, LDR, temperature sensor etc.

#### Course Contents

Module 1: Introduction to microprocessor and microcontroller, Introduction to Embedded system, Introduction to ARM Mbed platform.

Module 2: Simple programs like LED Blinking, use of SerialOut and SerialIn function.

Module 3: Basics of 7 segment display and LCD and their interfacing with breadboard.

Module 4: Basics of accelerometer, LDR, potentiometer and its interfacing with breadboard.

#### Who Should Attend

Any student from SE (EXTENSIVE) - First come first serve (Seat available only for 20 students-10 groups with 2 members each)

#### Speakers

Prof. Pushkar Sathe

Prof. Hema Raut

#### Registration Fees

Course Fees: FREE

(Cost of certificate will be applicable)

#### Contact for Registration

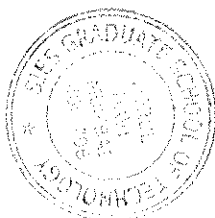
Prof. Pushkar Sathe

Mobile: 9870630637

Email ID : pushkar.sathe@siesgst.ac.in

Serial No.16

Event:C and C++ coding skills



Principal

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PLOT 10/11, SECTOR 7, MIDC  
PUNE-411 004, INDIA

SIES GRADUATE SCHOOL OF TECHNOLOGY  
PUNE-411 004, INDIA

Date: 26-12-17 to 28-12-17

Resource Person: Prof. Vivek Ramakrishnan

Nature: Regional



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

18-12-2017

IEEE SIESGST Student Branch and Department of EXTC are organizing a workshop on C Programming for you. The interested students can register the said event. The event details are given below.

Name of the event	Programming using C & C++ workshop
Date and Time	26-12-2017 to 28-12-2017, 10 AM to 4 PM
Eligibility	Open to all SE EXTC students
Contact Persons	Abhishek Singh 8425877338

*B. B. Balakrishnan*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A. N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10, SECTOR 5, NERUL, NAVIMUMBAI, MUMBAI-400706  
SECTOR 5, NERUL, NAVIMUMBAI, MUMBAI-400706

**Serial No.18**

**Event:** Aptitude Test and NPTEL Awareness Seminar

**Date:** 22-01-2018 , 23-01-2018 24-01-2018 29-01-2018

**Resource Person:** Prof. Siva Kumar

**Nature:** Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

15-01-2018

IEEE SIESGST Student Branch and Department of EXTC are organizing an aptitude test and NPTEL awareness seminar for you. The interested students can register the said event. The event details are given below.

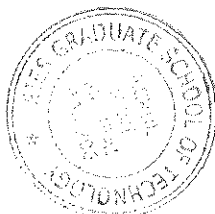
Name of the event	Aptitude Test and NPTEL Awareness Seminar
Date and Time	22-01-2018 for SE A at 401, 3.30 PM 23-01-2018 for SE B at 401, 3.30 PM 24-01-2018 for TE A at 401, 3.30 PM 29-01-2018 for TE B at 401, 3.30 PM
Eligibility	Open to all SE and TE EXTC students
Contact Persons	Athulya 9769011702 Simran 9987288306

*Dr Biju*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*

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S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/15, SECTOR 5, NAVIMUMBAI, MUMBAI-400706  
SECTOR 5, NAVIMUMBAI, MUMBAI-400706



Serial No.19

Event: Workshop on Antenna Design using IE3D

Date: 03-02-2018 , 17-02-2018 and 3-3-2018

Resource Person: Prof Shishir Jagtap, Prof Vandana Sawant

Nature: Regional

3 days Hands on Training on

### Antenna Design using IE3D

February 3rd and 17th of 2018 and March 3rd of 2018,

Timing :10.00 AM to 4.00 PM, Venue : Lab 316

Since the invention of the Microstrip Antenna four decades ago, the demand for its application has been increasing rapidly, especially within the last two decades. However, these applications have been in demand mostly by the Department of Defense. Because of their extremely thin profile (0.01 to 0.05 wavelength), printed Microstrip Antennas have found heavy applications in military aircraft, missiles, rockets, and satellites. The Microstrip Antenna, because of its small size, lightweight, low profile, and low manufacturing cost, is finding increasing applications in the commercial sector of the industry. This workshop aims at discussing briefly some of the antenna's technical design features and presents several important examples of the antenna's commercial applications, such as mobile satellite communications, direct broadcast satellite services, global positioning system, medical hyperthermia usage, etc.

#### Objectives

- Introduction to basic understanding and designing of the Patch Antenna.
- Simulation of the Patch Antenna using simulation software IE3D.
- To evolve, develop and improve different types of patch antennas suitable for numerous applications like microwave communication, radar, mobile communication, RFID applications etc.

#### Course Contents

- Introduction to Antenna and IE3D software
- Design, simulation and optimization of Rectangular Patch Antenna using IE3D software.
- Design and simulation of Rectangular Patch Antenna using different feeding techniques.
- Designing different types of patch antennas suitable for various applications.

#### Speakers

Prof. Shishir Jagtap  
Prof. Vandana Sawant

#### Who Should Attend

12 and 20 (ECC) students - first come first serve (Seats available only for 20 students - 10 groups with 2 members each)

#### Registration Fees

Course Fee: FREE  
(Cost of certificate will be applicable)

#### Contact for Registration

Prof. Vandana Sawant  
Email ID : vandana.sawant@psjps.ac.in



Principal  
P.S.J.P.S. GRADUATE SCHOOL  
PLOT 10/15, SECTOR 4,  
NOIDA

TECHNOLOGY  
NOIDA

**Serial No.20**

**Event:** Haptics- The Robotic arm Workshop

**Date:** 24-2-2018

**Resource Person:** Innovation Cell IITB and ROBOKART

**Nature:** Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Narul, Navimumbai-400706



## NOTICE

19-02-2019

IEEE SIESGST Student Branch and Department of EXTC are organizing a robotic arm workshop for you. The interested students can register the said event. The event details are given below.

Name of the event	Haptics- The Robotic arm Workshop
Date and Time	24-02-2018, 9 AM to 4 PM at Lab 304
Eligibility	Open to all students
Contact Persons	Athulya 9769011702 Simran 9987288306

*Biju Balakrishnan*

Prof. Biju Balakrishnan

IEEE SB Branch Counselor

& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC

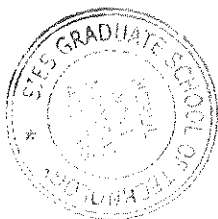
**Serial No.21**

**Event:** Seminar on Machine Learning and Roadbot

**Date:** 26-02-2018

**Resource Person:** IEEE,SIES GST

**Nature:** Regional



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/1/15 SECTOR 5, NAVIMUMBAI, MUMBAI-400706

TECHNOLOGY  
SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbal-400706



## NOTICE

19-02-2018

IEEE SIESGST Student Branch and Department of EXTC are organizing a Seminar on Machine Learning and Roadbot for you. The interested students can register for the same. The event details are given below.

Name of the event	Seminar on Machine Learning and Roadbot
Date and Time	26-02-2018, 3.30 PM at Room 304
Eligibility	Open to all students
Contact Persons	Nivedita Deshpande 7506656431

*Biju Balakrishnan*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC

### Serial No.22

**Event:** MIT App Inventor Workshop

**Date:** 20-03-2018

**Resource Person:** Mr. Aditya Gudal and Mr. Yuvaraj Mudaliyar

**Nature:** Regional



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/6, SRI CHANDRASEKARENDRASARASWATHY VIDYAPURAM  
SECTOR-5, NERUL, NAVI MUMBAI-400706



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

16-03-2018

IEEE SIESGST Student Branch and Department of EXTC are organizing a MIT App Inventor Workshop for you. The interested students can register the said event. The event details are given below.

Name of the event	MIT App Inventor Workshop
Date and Time	20-03-2018, 9.30 AM to 4.30 PM at CC
Eligibility	Open to all students
Contact Person	Aman Rajput 7709203550

*Dr Biju*

Prof. Biju Balakrishnan  
IEEE SB Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC

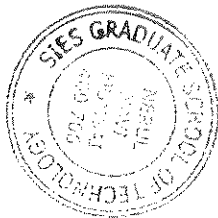
**Serial No.23**

**Event:** Seminar on Google Crowd Source

**Date:** 26-03-2018

**Resource Person:** IEEE SIES GST and Crowd Source Community

**Nature:** Regional



*[Signature]*  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/6, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706



SIES Graduate School of Technology  
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Sector 5, Nerul, Navimumbai-400706



## NOTICE

23-03-2018

IEEE SIESGST Student Branch and Department of EXTC are organizing a Seminar and Interaction on Training, Placements and Higher Studies for you. The interested students can register the said event. The event details are given below.

Name of the event	Seminar and Interaction on Training, Placements and Higher Studies
Date and Time	27-03-2018, 3.30 PM at GST Audi
Eligibility	Open to all TE EXTC students
Contact Persons	Abhishek Singh 8425877338

*B. B. Balakrishnan*

Prof. Biju Balakrishnan

IEEE SB Branch Counselor

& Event coordinator

*A. N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 104/5, SRI CHANDRASEKARENDRASARASWATHI VIDYAPURAM  
SECTOR 5, NERUL, NAVIMUMBAI, MUMBAI - 400706

**Serial No.24**

**Event:** Seminar and Interaction on Training, Placements and Higher Studies

**Date:** 27-03-2018

**Resource Person:** Dr. Vikram Patil

**Nature:** Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

24-03-2018

IEEE SIESGST Student Branch and Department of EXTC are organizing a Seminar and Interaction on Training, Placements and Higher Studies for you. The interested students can register for the same. The event details are given below.

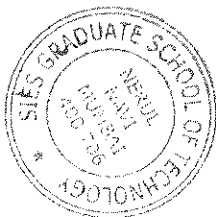
Name of the event	Seminar and Interaction on Training, Placements and Higher Studies
Date and Time	27-03-2018, 3.30 PM at CC
Eligibility	Open to all students
Contact Persons	Nivedita Deshpande 7506656431

*Dr Biju*

Prof. Biju Balakrishnan  
IEEE 5B Branch Counselor  
& Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar  
HoD, Dept. of EXTC



*[Signature]*  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/16, SRI CHANDRASEKENDRA SARASWATHI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706

Serial No.25

Event:-Antenna Design using IE3D

Date: 20-6-2018 To 22-6-2018

Resource Person: Prof Shishir Jagtap, Prof Vandana Sawant

Nature: Regional



**SIES Graduate School of Technology**  
Department of Electronics and Telecommunication Engineering  
Presents

3 days Hands on Training on

**Antenna Design using IE3D**

June 20<sup>th</sup> to 22<sup>nd</sup> June of 2018 **Timing: 10.00 AM to 4.00 PM.**  
**Venue : Lab 316**

Since the invention of the Microstrip Antenna four decades ago, the demand for its application has been increasing rapidly, especially within the last two decades. However, these applications have been in demand mostly by the Department of Defense. Because of their extremely thin profile (0.01 to 0.05 wavelengths), printed Microstrip Antennas have found many applications in military aircraft, missiles, rockets, and satellites. The Microstrip Antenna because of its small size, lightweight, low profile, and low manufacturing cost, is finding increasing applications in the commercial sector of the industry. This workshop aims at discussing briefly some of the antenna's technical design features and presents several important examples of the antenna's commercial applications, such as mobile satellite communications, direct broadcast satellite services, global positioning systems, medical hyperthermia usage, etc.

#### Objectives

- Introduction to basic understanding and designing of the Patch Antenna.
- Simulation of the Patch Antenna using simulation software IE3D.
- To evolve, develop and improve different types of patch antennas suitable for numerous applications like microwave communication, radar, mobile communication, RFID applications, etc.

#### Course Contents

- Introduction to Antenna and IE3D software
- Design, Simulation and Optimization of a Microstrip Patch Antenna using IE3D software.
- Design, Simulation and Optimization of a Microstrip Patch Antenna using different loading techniques.
- Designing different types of Patch antennas suitable for various applications.
- Design of Microstrip Monopole Antenna Array Antenna and Fabrication of Antenna.

#### Speakers

Prof. Shishir Jagtap,  
Prof. Vandana Sawant

#### Who Should Attend

20000 INR (INR) students - This course is free for all students (from 2000 INR only for 20 students - 10 groups with 2 members each)

#### Registration Fees

Course Fee: 20000



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/5, SIKS COMPLEX, VADAPUR, NAVI MUMBAI, DIST. THANE, MAHARASHTRA  
SACTUM-V, NERUR, NAVI MUMBAI - 401 202

### 3.1.3\_EXTC\_Year 2016-17

Serial No.7

Event: Workshop on “ARM mbed FRDM KL25Z”

Date: 4/1/2017 To 5/1/2017

Resource Person: Prof.Vishal Gaikwad

Nature: Regional



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

02-01-2017

IEEE SIESGST Student Branch and Department of EXTC are organizing a Workshop on ARM mbed FRDM KL25Z for you. The event details are given below. Interested students can register for the same.

Name of the event	Workshop on “ARM mbed FRDM KL25Z”
Date and Time	04-01-2017
Eligibility	Open to all EXTC students
Contact Person	Prof. Vishal Gaikwad, Event coordinator

A.N. Kemkar

Dr. Atul Kemkar

HoD, Dept. of EXTC



[Signature]

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/0/5, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706



**Serial No.8**

**Event:**

Virtual Appearance Seminar

**Date:** 25/06/2016

**Resource Person:** Mrs. Sunita Williams, Astronaut, NASA

**Nature:** Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

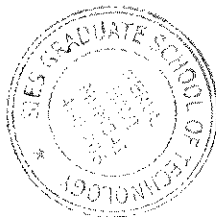
20-05-2016

IEEE SIESGST Student Branch and Department of EXTC are organizing a Virtual Appearance Seminar for you. The event details are given below. Interested students can register for the same.

Name of the event	Virtual Appearance Seminar by Mrs. Sunita Williams, Astronaut, NASA
Date and Time	25-06-2016
Eligibility	Open to all EXTC students
Contact Person	Prof. Pushkar Sathe, Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar



*Atul Kemkar*

Principal  
SIES GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/E, SRI CHANDRASEKENDRA SARASWATI VIDYAPURAM  
SECTOR 5, NERUL, NAVI MUMBAI - 400706

**Serial No.9**

**Event:**Hands on workshop on Drone making

**Date:** 27/08/2016

**Resource Person:** Apurva Godbole,Dinesh Sain,Prasanna Shevare,Co-founder and CEO

**Nature:** Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



**NOTICE**

20-02-2017

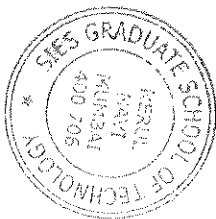
IEEE SIESGST Student Branch and Department of EXTC are organizing a workshop on electronics hacking and gesture control for you. The event details are given below. Interested students can register for the same.

Name of the event	Hands on Workshop on electronics hacking and Gesture control
Date	24-02-2017 and 25-02-2017
Eligibility	Open to all EXTC students
Contact Person	Prof. Pushkar Sathe, Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/6, SRI CHANDRASEKARENDRA SARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706

**Serial No.10**

**Event:**Hands on Workshop on electronics hacking and Gesture control

**Date:** 24-02-2017 To 25-02-2017

**Resource Person:** Kshitij Dadhekar,Karan Makharia,Founder and CEO Gettutorialized

**Nature:** Regional



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



**NOTICE**

22-08-2016

IEEE SIESGST Student Branch and Department of EXTC are organizing a workshop on drone making for you. The event details are given below. Interested students can register for the same.

Name of the event	Hands on workshop on Drone making
Date and Time	27-08-2016
Eligibility	Open to all EXTC students
Contact Person	Prof. Pushkar Sathe, Event coordinator

*A.N. Kemkar*

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/7/15, SECTOR 5, NERUL, NAVIMUMBAI-400706  
SECTOR 5, NERUL, NAVIMUMBAI-400706

**EXTC\_2015-16**

**Serial No.10**

**Event:Seminar on Amateur Radio**

**Date: 18-07-2015**

**Resource Person: Mr.Jayesh Banatwala, Head MARI(Mumbai Amateur Radio Institute).**

**Nature: Regional**



**SIES Graduate School of Technology**  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## NOTICE

13-07-2015

IEEE SIESGST Student Branch and Department of EXTC are organizing a Seminar on Amateur Radio for you. The event details are given below. Interested students can register for the same.

Name of the event	Seminar on Amateur Radio
Date and Time	18-07-2015
Eligibility	Open to all EXTC students
Contact Person	Prof. Pushkar Sathe, Event coordinator

A.N.Kemkar

Dr. Atul Kemkar

HoD, Dept. of EXTC



*[Signature]*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT IC/1/5, SRI CHANDRASEKARENDRASARASWATI VIDYAPURAM  
SECTOR-5, NERUL, NAVIMUMBAI-400706

**REGISTRATION FORM**  
AICTE-ISTE Approved One Week Short Term Training Course on

**"DevOps and R Programming"**  
December 16 - 21, 2019

Name: \_\_\_\_\_

Designation: \_\_\_\_\_

Department: \_\_\_\_\_

College: Address: \_\_\_\_\_

\_\_\_\_\_

Phone: \_\_\_\_\_

Email: \_\_\_\_\_

ISTE Membership Number: \_\_\_\_\_

\*ISTE membership is compulsory

Signature of Participant \_\_\_\_\_

Signature of Principal \_\_\_\_\_

**Important Dates:**

Registration Deadline: December 12, 2019

Course Duration: December 16 - 21, 2019  
(10 am to 5 pm)

**Who can apply:**

Faculty members, Industry persons, Research scholars and PG students from AICTE approved Engineering and Polytechnic Colleges.

Course Fee: Rs.2500/- (Includes certificate, course materials & tea)



Mode of payment : Demand Draft/ Cash/Cheque

**Details of Demand Draft:**

The Demand draft should be in favour of "Principal, SIES Graduate School of Technology" payable at Navi Mumbai.

**Resource Persons:**

Dr. Deven Shah, Vice Principal, TCET, Kandivali  
Dr. Bhushan Jadhav, TSEC, Bandra  
Prof. Mahendra Mehta, FRCRCE, Bandra  
Prof. Seema Redekar, SIES GST, Nerul  
Prof. Stuti Ahuja, SIES GST, Nerul



**AICTE-ISTE Approved**

**Short Term Training Program**

on

**DevOps and R Programming**

**December 16 - 21, 2019**

**Organized by**

**Department of Information Technology**

**SIES Graduate School of Technology**

Sri Chandrasekarendra Saraswathi Vidyapuram, Plot 1-C&E, Sector-5, Nerul, Navi Mumbai 400706  
Phone: 02261082400/0227171719  
[www.siesgst.edu.in](http://www.siesgst.edu.in)

**About SIES**

The South Indian Education Society (SIES) established in 1932 at Matunga is a pioneer in the field of education in this metropolis. The Society has been serving the cause of education and has carved for itself a niche as a provider of quality value based education from Nursery to Doctoral level in a wide variety of fields.

The Institutional Mission by our Founders: "This Society should sincerely serve the cause of education and the educational needs of the common man of this cosmopolitan city"

**About SIES Graduate School of Technology**

SIES Graduate School of Technology, an integral part of this well-established Community, started in the year 2002. The institute is located amidst the educational hub of Navi Mumbai and has been imparting quality technical education, offering four year Bachelor of Engineering courses in:

- Information Technology
- Electronics and Telecommunication Engineering
- Computer Engineering
- Printing & Packaging Technology
- Mechanical Engineering

The institute is approved by the All India Council for Technical Education, New Delhi, affiliated to the University of Mumbai and accredited by NAAC.

**Department of Information Technology**

The Department of Information Technology came into existence in 2002, and at present is operational with an intake of 60. It has progressed rapidly, to be regarded, as one of the premiere departments with excellent infrastructure. It has dedicated, highly qualified faculty members with excellent infrastructure and state of the art laboratories, in order to give the students a holistic and pragmatic view of the present scenario in Information Technology.

**The Course Objective**

- The objective of the STTP is to give in depth knowledge and hands on training of the newly introduced subject "Devops and R-programming" in B.E.(I.T.) Sem VIII, Mumbai University.

**The Course Contents**

- Concept of Devops with related technologies which are used to code, build test, configure and monitor the software applications.
- Version control(RCS/CVS/GIT/Mercurial)
- Continuous integration using Jenkins
- Infrastructure monitoring using Nagios
- Docker - containerization
- Software Configuration Management and provisioning using Ansible and Puppet.
- Basics of R-programming and Analytics using R.

**The Course Outcome**

Participants will be able to,

- Understand concept of DevOps with associated technologies.
- Understand different version Control tools like GIT, CVS.
- Understand Docker to Build, ship and run.

**Chief Patrons**

Dr. V. Shankar  
Hon. President, SIES.

Shri. M. V. Ramnarayan  
Hon. Secretary, SIES.

Dr. P. V. Parameswaran  
Hon. Advisor, SIESGST.

**Conveners**

Dr. Atul Kenkar, Principal  
Dr. K. Lakshmisudha, HOD (IT)

**Course - Conveners**

Prof. Seema Redekar  
Prof. Stuti Ahuja

**Coordinators**

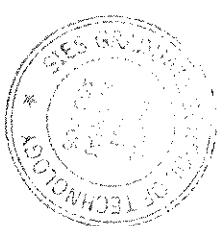
Prof. Savita Lohiya  
[Savita.lohiya@siesgst.ac.in](mailto:Savita.lohiya@siesgst.ac.in)  
Mobile: +919224366582

Prof. Saritha L. R.  
[saritha.r@siesgst.ac.in](mailto:saritha.r@siesgst.ac.in)  
Mobile: +917738382483

**Organizing Committee:**

Prof. Leena Ladge  
Prof. Mrinal Khadse  
Prof. Bushra Shaikh

Register here:  
<https://forms.gle/hsaBrEYVYkxblHcb17>



*(Handwritten Signature)*

Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/5, SECTOR 5, NERUL, NAVI MUMBAI 400706

TECHNOLOGY  
SRI CHANDRASEKARENDRASARASWATHI VIDYAPURAM

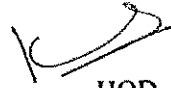
30 ) Artificial Intelligence 21 Sep 2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

17/09/2019

All the students of BE IT are informed that we are organizing Artificial Intelligence Seminar for BE students on 21<sup>st</sup> September 2019. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chandrasekarendra Saraswathy Vidyapuram  
Plot-1-C&E, Sector-V, Nerul, Navi Mumbai-400705



Principal  
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PLOT 1C/D/3, SECTOR-V, NERUL, NAVI MUMBAI-400705  
SRI CHANDRASEKARENDRASARASWATHY VIDYAPURAM



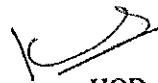
32) Industrial IoT

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

**02/10/2019**

All the Third Year students are informed that Expert talk on "Expert Talk on Industrial IoT" is scheduled on 10/10/2019 at 2.30 P.M. Attendance is compulsory.



HOD

**HEAD Dr. K. Lakshmisudha**  
Department of Information Technology  
S. J. E. S. Graduate School of Technology  
Sri Chandrasekarendra Saraswathy Vidyapuram  
Plot-1-C&E, Sector-V, Nerul, Navi Mumbai-400706



Principal  
S.J.E.S. G.S.T.  
PLOT 1C&E  
SECTOR V, NERUL

DEPARTMENT OF INFORMATION TECHNOLOGY  
SRI CHANDRASEKARENDRASARASWATHY VIDYAPURAM

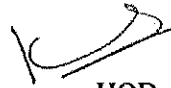
36) Expert Talk on “Angular JS and NoSQL”

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

**30/09/2019**

All the Third Year students are informed that Expert Talk on “Angular JS and NoSQL” is scheduled on 05/10/2019 at 9.00 A.M. Attendance is compulsory.



HOD

**HEAD Dr. K. Lakshmisudha**  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chandrasekarendra Saraswathy Vidyapuram  
Plot-1-C&E, Sector-V, Nerul, Navi Mumbai-400705



Principal

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SECTOR-V, NERUL, NAVI MUMBAI - 400 705.





27) Bootstrap Framework 31-07-19-06-08-19

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

10/01/2019

All the students of SE, TE and BE are informed that we are organizing training Program on "Bootstrap Framework" on 31-07-2019 to interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidysapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



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PLOT 1C/E/S, SRI CHONDRASEKARENDRASARASWATHY VIDYAPURAM  
SECTOR-V, 400706, NAVI MUMBAI - INDIA



## 28) CCNA-Routing and Switching(40 hr) 27-06-2019 to 31-08-2019

### **SIES Graduate School of Technology**

Department of Information Technology

Presents

**5 days Hands on CCNA Routing and Switching - 1,  
June 27<sup>th</sup> – 2<sup>nd</sup> July, 9:00 AM to 5:00 PM**

This certification program is designed to impart knowledge about the fundamentals of Computer Networks, various protocols used in Communication, Managing and configuring Cisco Switches and Routers. It is also designed to give exposure on building a Network.

#### **Objectives**

1. To learn basics of Networking.
2. To design a Network using switches and Routers.
3. To discover problems in Routing, password recovery etc.

#### **Course Contents**

**Module 1:** Introduction

**Module 2:** Understanding, configure Router and Switch.

**Module 3:** Implementation of DHCP, ACL, NAT, PAT etc.

**Module 4:** Password Recovery, Backup of running config to startup config.

#### **Who Should Attend**

Any student from TE (EXTC/IT/CE)  
(Students who have completed their IV SEM Exams)

#### **Registration Fees**

Course Fees: FREE

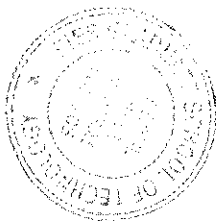
(Cost of certificate will be applicable)

#### **Resource Person**

Dr. K. Lakshmi Sudha

Mobile: 9987512269

Email ID: lakshmi.sudha@siesgst.ac.in



**Principal**

**S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY**  
PLOT 10/A/5, SION (WEST), MUMBAI - 400 081  
SECTOR-V, NORTH, NEVINSARAI, MUMBAI

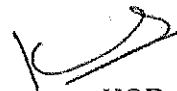
31) Designing Java Applications with Database Connectivity 24-09-2019.

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

**17/09/2019**

All the Second Year students are informed that Seminar on “Java applications with database connectivity” is scheduled on 24/09/2019. Attendance is compulsory.



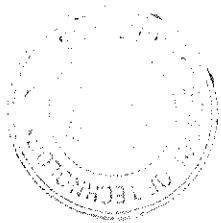
HOD

**HEAD Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidyapuram  
Plot-1-C&E, Sector-V, Nerul, Navi Mumbai-400706**



Principal

**S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C&E, SRI CHONDRASEKARENDRASARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, NAVI MUMBAI-400706**



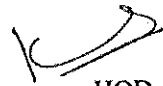
26) Enterprise Network Design using L2 andL3 switch 26-08-19.

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DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

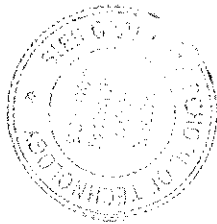
**19/08/2019**

All the Final Year students are informed that Workshop on “Enterprise Network Design using L2 andL3 switch” is scheduled on 26/08/2019. Attendance is compulsory.



HOD

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S. I. E. S. Graduate School of Technology  
Sri Chandrasekarendra Saraswathy Vidyapuram  
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SECTOR-V, NERUL, NAVI MUMBAI-400705

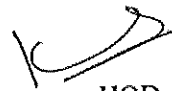
33) Career Enlighten on Networking and Cyber Security 14-10.2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

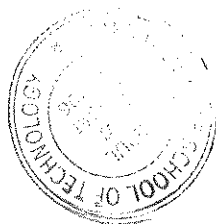
07/10/2019

All the BE students are informed that Workshop on "Career Enlighten on Networking and Cyber Security" is scheduled on 14/10/2019. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chandrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400705



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/E/5, CHANDRASEKARENDRASARASWATHY VIDYAPURAM  
SECTOR-V, NAVI MUMBAI-400705

35) Current Trends and Requirement in Industry Ms. Stuti Ahuja August 29, 2019  
(Photos are also required)

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

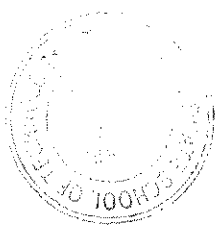
22/08/2019

All the Third Year students are informed that Expert Talk on "Current Trends and Requirement in Industry" is scheduled on 29/08/2019. Attendance is compulsory.



HOD

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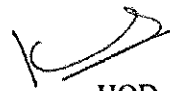
34) Software Testing Tools, an Introduction to popular industry oriented tools" 9/25/2019

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DEPARTMENT OF INFORMATION TECHNOLOGY**

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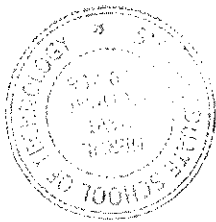
**09/09/2019**

All the Final Year students are informed that Expert Talk on "Software Testing Tools, an Introduction to popular industry oriented tools" is scheduled on 16/09/2019. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
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S. I. E. S. Graduate School of Technology  
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Principal

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TECHNOLOGY  
SRI CHONDRASEKARENDRASARASWATHY VIDYAPURAM

2018-2019

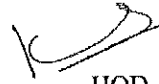
53) Importance of Simulation in Industry 28/03/2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

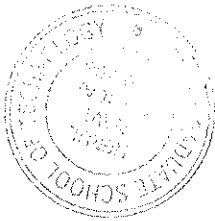
25/03/2019

All the Final Year students are informed that we are organizing Seminar on  
"Importance of Simulation in Industry" on 28/03/2019 at 12.00 P.M.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidyapuram  
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Principal  
S. I. E. S. Graduate School of Technology  
Plot 1C/E/S, Sector-V, Nerul, Navi Mumbai-400706  
SECTOR-V, NAVI MUMBAI, DIST. MUMBAI



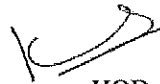
44) Digital Market by Social Kinnect 14/02/2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

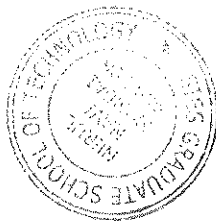
**10/01/2019**

All the Final Year students are informed that "Awareness Session On Digital Market by Social Kinnect" is scheduled on 14/01/2019. Attendance is compulsory.



HOD

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Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Bri Chondrasekarendra Saraswathy Vidyapuram  
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S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C&E, SECTOR-V, NERUL, NAVI MUMBAI-400706

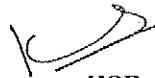
55) Expert talk on “cyber crime, Digital Forensics and Career Opportunities” 8/4/2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

03/04/2019

All the Third Year students are informed that Expert talk on “cyber crime, Digital Forensics and Career Opportunities” is scheduled on 08/04/2019 at 1.30 P.M. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chandrasekarendra Saraswathy Vidyapuram  
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S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C&E, SECTOR-V, NERUL, NAVI MUMBAI-400706


54) Alumni Interaction on Storage Network Managemnet Retraival 4/04/19

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

01/04/2019

All the Final Year students are informed that Seminar on "Storage Network Management Retrieval" is scheduled on 04/04/2019 at 11.00 A.M. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Infomation Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidyapuram  
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Principal

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SRI CHONDRASEKARENDRASARASWATHY VIDYAPURAM  
PLOT-1-C & E, SECTOR-V, NERUL, NAVI MUMBAI-400705

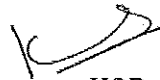
64) Electronics domain & its importance in IOT (TE IT) 04/09/18

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

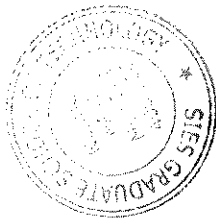
**28/08/2018**

All the Third Year students are informed that workshop on "Electronics domain & its importance in IOT" is scheduled on 04/09/18. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidyapuram  
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PLOT 1C/E, SRI CHANDRASEKARENDRA SARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, NAVI MUMBAI-400706

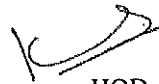
65) Electronics domain & its importance in IT (SE IT) 04/09/18

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

**27/08/2018**

All the Second Year students are informed that workshop on "Electronics domain & its importance in IOT" is scheduled on 04/09/18. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



Principal  
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SRI CHONDRASEKARENDRASARASWATHY VIDYAPURAM  
PLOT-1-C & E, SECTOR-V, NERUL, NAVI MUMBAI-400706

## 66) Designing and Managing Database 11-06-18-15-06-18

### SIES Graduate School of Technology

Department of Information Technology

Presents

5 days Hands on Training

on

### Designing and Managing Databases

11 to 15 June 2018  
(9.30 AM to 4.00 PM)

This student development program is meant to revise basics concepts of Database Management System and to design the relational model for a given problem. This program mimics the development of a "real" database by designing a conceptual model in the form of an Entity Relationship diagram and translating the conceptual model into a relational schema.

#### Objectives

- Revise basics of Database Management concepts.
- Implementing the relational schema in PostgreSQL.
- Populating the database.
- Querying and modifying the database by SQL statements.

#### Course Contents

Module 1: Fundamentals Database: Concepts, the Entity Relationship (ER) Model and EER model  
Module 2: DDL and DML statements, Integrity constraints, Aggregation, Joins, Nesting of Queries.  
Module 3: Views and Transaction Control Commands.  
Module 4: Triggers, Stored procedures and functions.

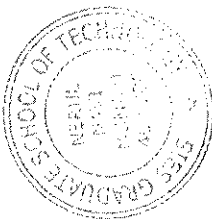
#### Mini Project Implementation

##### Resource Persons:

1. Prof. Savita Lohiya
2. Prof. Saritha L.R.
3. Prof. Bushra Shaikh
4. Prof. Stuti Ahuja

#### Who Should Attend

Any students from SE, TE IT/CE- First come first serve (Seat available only for 20 students)



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/11/15, 5TH CROSS, 1ST STAGE, SWASTHI VIKAS NEAR  
SECTOR-W, WERUL, KATOL, PUNE-411 004

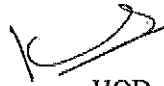
46) Expert Talk on Spark and Hadoop for BDA Subject for BE Students 7/2/2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

04/02/2019

All the Final Year students are informed that Expert Talk on "Spark and Hadoop" is scheduled on 07/02/2019 at 9.00 A.M. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidyapuram  
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S. I. E. S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1-C & E, SECTOR-V, NERUL, NAVI MUMBAI-400706

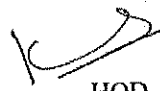
57) Responsive Web Page Design using Bootstrap 4 7/21/2018

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

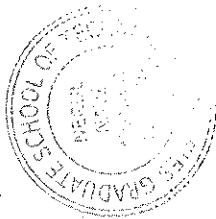
**16/06/2018**

All the Third Year students are informed that we are organizing SDP on "Responsive Web Page Design using Bootstrap 4" on 21<sup>st</sup> June 2018. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
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S. I. E. S. Graduate School of Technology  
Bri Chondrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



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PLOT 1C/D/E, SECTOR-V, NERUL, NAVI MUMBAI-400706  
BRI CHONDRASEKARENDRASARASWATHY VIDYAPURAM



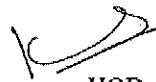
63) Softskill & Industry Interaction 8/3/2018

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

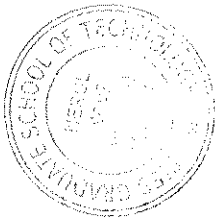
**30/07/2018**

All the Second Year students are informed that Seminar on "Soft skill & Industry Interaction" is scheduled on 03/08/18. Attendance is compulsory.



HOD

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Sri Chandrasekarendra Saraswathy Vidyapuram  
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PLOT 1C/E/25, SECTOR-V, NERUL, NAVI MUMBAI-400706

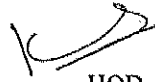
67) Digital Marketing & E-Commerce 10/1/2018

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

**25/09/2018**

All the TE and BE students are informed that Seminar on "Digital Marketing & E-Commerce" is scheduled on 01/10/18. Attendance is compulsory.



HOD

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Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Bri Chondrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



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S.I.E.S. Graduate School of Technology  
Plot 1-C & E, Sector-V,  
Nerul, Navi Mumbai-400706

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PLOT-1-C & E, SECTOR-V, NERUL, NAVI MUMBAI-400706

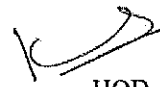
47) Healthy Mind 2-18-2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

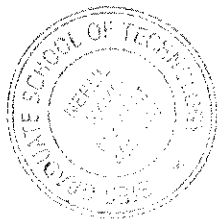
**12/02/2019**

All the SE and BE students are informed that Seminar on "Healthy Mind" is scheduled on 18/02/2019 at 3.30 P.M. Attendance is compulsory.



HOD

**HEAD Dr. K. Lakshmisudha**  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



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S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT-1-C & E, SECTOR-V, NERUL, NAVI MUMBAI-400706

## 60) Machine Learning with R 25-07-18-29-07-18

### SIES Graduate School of Technology

5 days Hands on Training

on

### Machine Learning with 'R'

under

Center of Excellence In Data Science and Deep learning

June 25-29, 2018

(9.30 AM to 4.00 PM)

By the end of this course, students will be able to identify the difference between a supervised (classification) and unsupervised (clustering) technique, identify which technique they need to apply for a particular dataset and need, engineer features to meet that need, and write 'R' code to carry out an analysis.

#### Objectives

- Understand components of a machine learning algorithm.
- Develop a basic understanding of the principles of machine learning and derive practical solutions using predictive analytics.
- Use data patterns to make decisions and predictions with real-world examples

#### Course Contents

**Module 1:** What is Machine Learning?

**Module 2:** Performance measures

**Module 3:** Classification, Regression – Linear and logistics

**Module 4:** Clustering

**Module 5:** Prediction, Support Vector Machines

**Mini Project Implementation**

#### Resource Persons:

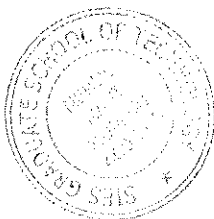
1. Prof. Pranita Mahajan
2. Prof. Varsha Mali
3. Prof. Seema Redekar

#### Who should attend?

1. **Prerequisite:** Basic "R Language" and Basic Statistics
2. Students from SE/TE (All Branches) –First Come First Serve( 20 seats)

#### Registration Fees

**Course Fees: FREE**  
(Cost of certificate will be applicable)



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S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/6, SECTOR 10, MIDC, WADGAON, WADGAON VILHAPURAM  
SECTOR 10, MIDC, WADGAON, WADGAON VILHAPURAM


48) Digital Marketing Skill Development 2-28-2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

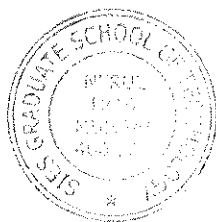
25/02/2019

All the Third Year students are informed that Workshop on "Digital Marketing Skill Development" is scheduled on 28/02/2019. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
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Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400705



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PLOT 1C/E, SECTOR-V, NERUL, NAVI MUMBAI-400705  
SECTOR-V, NERUL, NAVI MUMBAI-400705

68) Google Dorking & Keylogger 05/10/2018

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

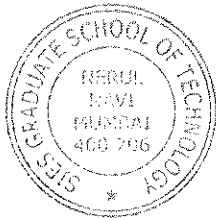
1/10/2018

All the TE IT students are informed that we are organizing seminar on "Google Dorking & Keylogger" on 05/10/2018. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chandrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDRASEKARENDRASARASWATHY VIDYAPURAM  
SECTION-V, NERUL, NAVI MUMBAI-400706

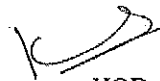
45) Technical talk on "Networking and Wireless LAN fundamentals" for Wireless Network subject 05/02/2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

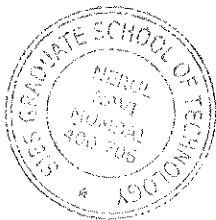
**28/01/2019**

All the Third Year students are informed that Technical talk on "Networking and Wireless LAN fundamentals" is scheduled on 05/02/2019. Attendance is compulsory.



HOD

**HEAD Dr. K. Lakshmisudha**  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



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S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/E, SRI CHANDRASEKARENDRA SARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, NAVI MUMBAI-400706

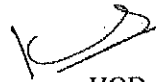
51) Expert talk on " Trending Technology in IT" 25/03/2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

18/03/2019

All the Third Year students are informed that Expert talk on "Trending Technology in IT" is scheduled on 25/03/2019. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chandrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



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PLOT 1C/E, SECTOR-V, NERUL, NAVI MUMBAI-400706  
SRI CHANDRASEKARENDRASARASWATHY VIDYAPURAM



## 59) Introduction to R 20-07-18-22-07-18

### **SIES Graduate School of Technology**

3 days Hands on Training

on

### **Introduction to 'R' for Data Science**

under

**Center of Excellence in Data Science and Deep learning**

June 20-22, 2018

(9.30 AM to 4.00 PM)

This student development program is meant to master the basics of this beautiful open source language "R programming". With the knowledge gained in this course, students will be ready to undertake first very own data analysis project.

#### **Objectives**

- To implement various applications using R.
- To make efficient use of R packages.
- To develop an end-to-end analytics solution using R language.
- Students will receive certificate post successful completion of workshop.

#### **Course Contents**

**Module 1:** Introduction to R, Installation and Setup

**Module 2:** Basic Data Structures in R

**Module 3:** Introduction to Functions and Packages in R

**Module 4:** Importing and exporting datasets in R, Use of R for data analytics

**Module 5:** Data Visualization with R

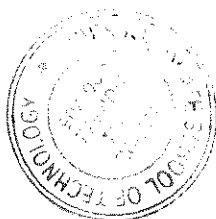
**Mini Project Implementation**

#### **Resource Persons:**

1. Prof. Pranita Mahajan
2. Prof. Amit Pandhare
3. Prof. Bushra Shaikh

#### **Who should attend?**

Students from FE/SE/TE (All Branches) –First Come First Serve basis (20 seats)



Principal  
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PLOT 1C/D/E, SION ROAD,  
SECTOR-V, LEADS, MUMBAI - 400 022

## 61) Machine Learning with Python 25-07-18-29-07-18

### SIES Graduate School of Technology

5 days Hands on Training

on

### Machine Learning with Python

under

Center of Excellence in Data Science and Deep learning

June 25-29, 2018

(9.30 AM to 4.00 PM)

By the end of this course, students will be able to identify the difference between a supervised (classification) and unsupervised (clustering) technique, identify which technique they need to apply for a particular dataset and need, engineer features to meet that need, and write python code to carry out an analysis.

#### Objectives

- Extraction, transformation, scraping, joining and cleaning of large data sets
- Hands-on knowledge on Machine learning concepts in Python using problem solving approach by working on real time cases and in class programming assignments
- Use machine learning libraries and apply established machine learning algorithms in Python

#### Course Contents

Module 1: Numpy , Pandas

Module 2: Fundamentals of Machine Learning - Intro to SciKit Learn

Module 3: Supervised Machine Learning

Module 4: UnSupervised Machine Learning

Module 5: Neural Networks

Mini Project Implementation

#### Resource Persons:

1. Dr. Vijay Katkar
2. Prof. Varsha Mali
3. Prof. Roopal Manuora
4. Prof. Swati Rane

#### Who should attend?

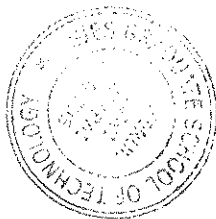
1. Prerequisite: Basic python and Basic Statistics
2. Students from PE/SE/TE (All Branches) –First Come First Serve( 20 seats)

#### Registration Fees

Course Fees: FREE

(Cost of certificate will be applicable)

#### Contact:



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/10B, SION ROAD, PUNE  
SECTOR-4, WARD-10, PUNE-411 005

## 62)Machine Learning with Python 17-12-2018 - 22-12-2018

### SIES Graduate School of Technology

5 days Hands on Training

on

### Machine Learning with Python

under

Center of Excellence in Data Science and Deep learning

Dec 17-21, 2018

(9.30 AM to 4.30 PM)

By the end of this course, students will be able to identify the difference between a supervised (classification) and unsupervised (clustering) technique, identify which technique they need to apply for a particular dataset and need, engineer features to meet that need, and write python code to carry out an analysis.

- Extraction, transformation, scripting, joining and cleaning of large data sets
- Hands-on knowledge on Machine learning concepts in Python using problem solving approach by working on real time cases and in class programming assignments
- Use machine learning libraries and apply established machine learning algorithms in Python

**Module 1:** Data Cleaning and Preprocessing

**Module 2:** Fundamentals of Machine Learning - Intro to SciKit Learn

**Module 3:** Supervised Machine Learning- Classification

**Module 4:** Regression

**Module 5:** UnSupervised Machine Learning

**Mini Project Implementation**

#### Resource Persons:

1. Prof. Prunita Mahajan
2. Prof. Mrinal Khadse
3. Prof. Varsha Patil
4. Prof. Vaishali Bhujade
5. Prof. Swati Rane
6. Prof. Shyamala Mathi

1. **Prerequisite:** Basic python and Basic Statistics
2. Students from SE/TE/BE (All Branches) –First Come First Serve( 20 seats)



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY

PLOT 10/15, SION (EAST), PUNE

SECTOR 4, WARD 10, PUNE

SIES GRADUATE SCHOOL OF TECHNOLOGY

PLOT 10/15, SION (EAST), PUNE

SECTOR 4, WARD 10, PUNE

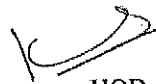
66) Career Development 9/6/2018

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

**30/08/2018**

All the Second Year students are informed that Seminar on "Career Development" is scheduled on 06/09/18. Attendance is compulsory.



HOD

**HEAD Dr. K. Lakshmisudha**  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
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Principal  
S.I.E.S. Graduate School of Technology  
Plot 1C/1E, Sector-V, Nerul, Navi Mumbai-400706

DEPARTMENT OF INFORMATION TECHNOLOGY  
SRI CHONDRASEKARENDRASARASWATHY VIDYAPURAM

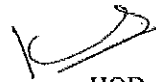
43) Web Application Development using Django Framework 19/01/2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

14/01/2019

All the Second Year students are informed that workshop on "Web Application Development using Django Framework" is scheduled on 19/01/2019.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Bri Chandrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



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PLOT 1C/D/E, SECTOR-V, NERUL, NAVI MUMBAI-400706

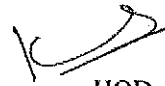
49) Seminar on "Server and Infrastructure setup using Linux" for SE IT 16/03/2019

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

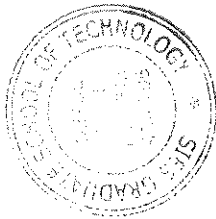
**11/03/2019**

All the Second Year students are informed that Seminar on "Server and Infrastructure setup using Linux" is scheduled on 16/03/2019 at 12.00 P.M. Attendance is compulsory.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. I. E. S. Graduate School of Technology  
Sri Chondrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



Principal  
S.I.E.S. ROAD  
PLOT 1C/E, SECTOR V  
NERUL, NAVI MUMBAI

TECHNOLOGY  
SRI CHONDRASEKARENDRA SARASWATHY VIDYAPURAM



1) One week AICTE-ISTE approved STTP on Computer Network and Security (in collaboration with IT department) 15-11-2018-22-11-2018  
58) Basic Python for data science (20-07-18-22-07-18)

## SIES Graduate School of Technology

3 days Hands on Training

on

### Basic Python for Data Science

under

Center of Excellence in Data Science and Deep learning

June 20-22, 2018

(9.30 AM to 4.00 PM)

This student development program is meant to master the basics of this open source language 'Python'. With the knowledge gained in this course, students will be ready to undertake first very own Python project for Data analysis

#### Objectives

- To understand different data types, decision making statements and Functions in Python
- Interpret Object oriented programming in Python
- Understand and summarize different data handling, Manipulation, analysis operations

#### Course Contents

**Module 1:** Introduction to Python , Installation and Setup  
**Module 2:** Basic Datatypes in Python, Decision Making statements and Functions  
**Module 3:** Introduction to Functions and Packages in Python  
**Module 4:** Object oriented programming in Python  
**Module 5:** Numpy, Export/Import data in Python  
**Mini Project Implementation**

#### Resource Persons:

1. Prof. Mrinal Khadse
2. Prof. Shyamala Mathi
3. Prof. Vaishali Bhujade

#### Who should attend?

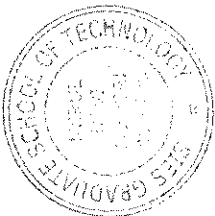
Students from FE/SE/TE (All Branches) –First Come First Serve basis( 20 seats)

#### Registration Fees

Course Fees: FREE  
(Cost of certificate will be applicable)

#### Contact:

Prof. Mrinal Khadse



Principal  
S.I.E.S. G.S.T.  
PLOT 10/10E,  
SECTOR 10, WARANANAGAR

TECHNOLOGY  
WARANANAGAR



85)One week AICTE-ISTE approved STTP on "Neural Network and Deep Learning use Tensor Flow"

**SIES Graduate School of Technology**  
Department of Information Technology  
6 days Hands on Training  
on  
**DEEP LEARNING WITH TENSORFLOW**  
under  
Center of Excellence in Data Science and Deep learning

June 26- July 02, 2018  
(9.30 AM to 4.30 PM)

**What you will Learn**

The components of a deep neural network and how they work together. A working knowledge of vocabulary, concepts, and algorithms used in deep learning. The basic types of deep neural networks (MLP, CNN, RNN) and the type of data each is designed for

**How to build:**

- An end-to-end model for recognizing hand-written digit images, using a multi-class Logistic Regression and MLP (Multi-Layered Perceptron), MLP for AUDIO
- A CNN (Convolution Neural Network) model for improved digit recognition and CNN for audio

**Course Content:**

**Day 1:** What is a neural network?

Supervised Learning with Neural Networks

**Day 2:** Introduction to Tensorflow.

**Day 3:** MLP with Tensor flow, Case study on handwritten Character Recognition using MLP

**Day 4:** CNN using Tensorflow, Case study on Audio classification

**Day 5:** RNN , Case on RNN

**Day 6 :** Mini Project Implementation

**Resource Persons:**

1. Dr. Vijay Katkar
2. Prof. Varsha Mali

**Registration Fees**

Course Fees: FREE

(Cost of certificate will be applicable)

**Contact:**

Prof. Varsha Mali

Email: varsha.mali@siesgst.ac.in

Mobile No: 9870744215



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TECHNOLOGY  
MIDC, PUNE

2017-2018

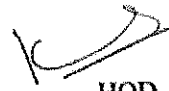
33) Gap bridging seminar on Project Guidance Innovative Project Ideas & Guidance for TE IT students

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DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

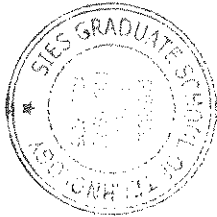
**30/03/2018**

All the students of TE are informed that we are organizing seminar on Project Guidance Innovative Project Ideas & Guidance for TE IT students on 4<sup>th</sup> April 2018. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



**HOD**

**HEAD Dr. K. Lakshmisudha**  
**Department of Information Technology**  
**S. I. E. S. Graduate School of Technology**  
**Sri Chandrasekarendra Saraswathy Vidyapuram**  
**Plot-1-C&E, Sector-V, Nerul, Navi Mumbai-406706**



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**Plot 1C/E, Sri Chandrasekarendra Saraswathy Vidyapuram**  
**SECTOR-V, (SIES) NERUL, NAVI MUMBAI-406706**

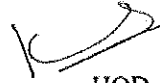
## 26)SDP on Web Application Development using Django Framework

### SIES GRADUATE SCHOOL OF TECHNOLOGY DEPARTMENT OF INFORMATION TECHNOLOGY

#### Notice

15/01/2018

All the students of SE / TE/ BE are informed that we are organizing Student Development Program on topic on Web Application Development using Django Framework on 20<sup>th</sup> January 2018. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

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8ri Chandrasekarendra Saraswathy Vidyapuram  
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S.I.E.S. G.T.  
PLOT 1C/2/3, S.  
SECTOR-V, NERUL, NAVI MUMBAI-400706

TECHNOLOGY  
SARASWATHY VIDYAPURAM

## 28) Python Programming SDP



**SIES Graduate School of Technology**  
**Department of Computer Engineering**  
**And Information Technology**  
Organizing  
**Hands on Training on Python Programming**  
From 20 Jan 2018 [Every Saturday: 9.30 AM to 5.00 PM]

Python is open source programming language, with characteristics such as easy to learn, portable, platform independent, supports procedural as well as object oriented language. Python is great for backend web development, data analysis, artificial intelligence, and scientific computing. Now a day's many developers have also used Python to build productivity tools, games, and desktop apps.

#### Objectives:

- Develop a basic understanding of Python programming language.
- Build simple web applications that include a database to manage user data.
- Effective use of common developer tools and implementation of best practices to write professional-quality code.
- Creation of programming projects from scratch using in-demand skills and technologies.

#### Course Contents:

- Introduction to basic python programming
- File Handling
- Database Connectivity
- Graphical User Interface
- Network Programming
- Mini Project

#### Who Should Attend:

Any student from TE (All branches)

#### Outcomes:

- This SDP will give insight to all aspect of Python programming language
- This SDP will help the students to develop applications in various domains

#### Contact for Registration:

Prof. Masooda Modak  
9833161007



Principal

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WIDYAVIHAR, WADGAON

WADGAON, WADGAON

TECHNOLOGY  
WIDYAVIHAR, WADGAON

### 30) SDP on "Advanced Java" for TE students of IT/CE/EXTC



## SIES Graduate School of Technology

Department of Information Technology

Presents

5 days Hands on Training on

**Advanced Java**

18-22, December, 9:00 AM to 5:00 PM

This student development program is meant to revise fundamentals of Object Oriented Programming and to learn Advanced Java concepts. It is also designed to give exposure on building a Java based applications.

#### Objectives

1. Revise basic Java concepts
2. Design and connecting frontend with backend
3. Learning Advance Java topics

#### Course Contents

**Module 1:** Revision of Basic Java concepts.

**Module 2:** Design Graphical User Interface using Applet, AWT and Swings.

**Module 3:** Java database connectivity using JDBC.

**Module 4:** Introduction to Hibernate, Spring, Struts.

#### Who Should Attend

Any student from TE (EXTC/IT/CE)

#### Registration Fees

Course Fees: **FREE**

(Cost of certificate will be applicable)



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SECTOR-8, CHANDRAPURAM

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PLOT 10/A, P.E. ROAD,  
SECTOR-8, CHANDRAPURAM

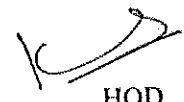
31) seminar on Middleware and Integration

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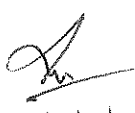
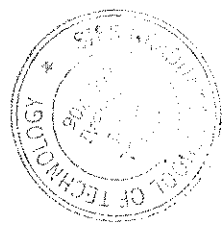
15/03/2018

All the students of TE IT are informed that we are organizing seminar on Middleware and Integration on 26<sup>th</sup> March 2018. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

**HEAD** Dr. K. Lakshmisudha  
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Sri Chandrasekarendra Saraswathy Vidyapuram  
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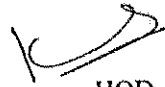
32) Gap bridging seminar on Software Testing Tools for BE IT students

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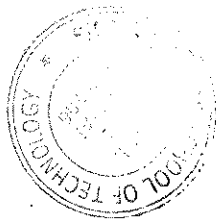
**15/03/2018**

All the students of BE IT are informed that we are organizing seminar on Software Testing Tools on 27<sup>th</sup> March 2018. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

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29) SDP on Image Processing with R for BE students

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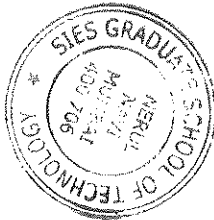
31/07/2017

All the students of BE IT are informed that we are organizing seminar on Software Testing Tools on 4<sup>th</sup> and 5<sup>th</sup> August 2017. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

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PLOT 1C/D/E, SRI CHANDRASEKARENDRA SARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, NAVI MUMBAI - 400705



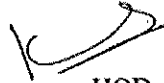
35) Gap bridging seminar on "Security in Linux" for SE IT students

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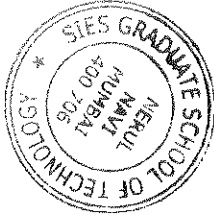
31/03/2018

All the students of TE IT and SE IT are informed that we are organizing seminar on Security in Linux on 5<sup>th</sup> April 2018. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



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Sri Chondrasekarendra Saraswathy Vidyapuram  
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36) Gap bridging seminar on "Security in Linux" for TE IT students

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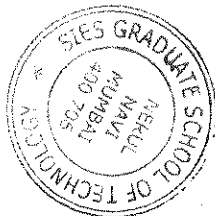
**31/03/2018**

All the students of TE IT and SE IT are informed that we are organizing seminar on Security in Linux on 5<sup>th</sup> April 2018. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

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Bri Chandrasekarendra Saraswathy Vidyapuram  
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37) Higher Studies Awareness Seminar for TE IT

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

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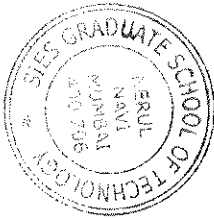
07/04/2018

All the students of TE IT are informed that we are organizing Higher Studies Awareness Seminar for TE IT on 13<sup>th</sup> April 2018. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

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38)Gap bridging seminars for the subjects - DSA

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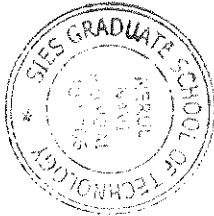
**20/10/2017**

All the students of SE IT and TE IT are informed that we are organizing Gap bridging seminars for the subjects - DSA, on 24<sup>th</sup> October 2017. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

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**39)Alumni Talk on Digital Marketing, Industrial Experience& Career Guidance**

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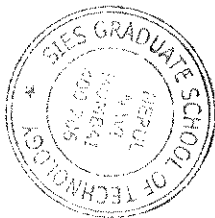
**31/05/2018**

All the students of BE IT are informed that we are organizing Alumni Talk on Digital Marketing, Industrial Experience& Career Guidance on 6<sup>th</sup> June 2018. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



**HOD**

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**Principal**

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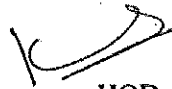
40) Latex Workshop for BE students

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DEPARTMENT OF INFORMATION TECHNOLOGY**

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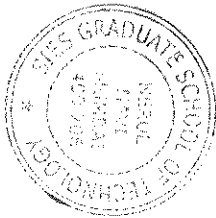
2/09/2017

All the students of B|E IT are informed that we are organizing Latex Workshop for BE students on 8<sup>th</sup> September 2017. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



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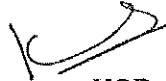
41)Hybrid App Development

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**Notice**

10/10/2017

All the students of BE IT are informed that we are organizing Hybrid App Development Workshop for BE students on 15<sup>th</sup> October 2017. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



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2016-2017

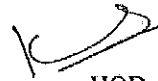
12. Ethical Hacking 20-08-2016 -21-08-2016

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**Notice**

15/8/2016

All the students are informed that we are organizing Ethical Hacking Workshop for all students from 20<sup>th</sup> August 2016 to 21<sup>st</sup> August 2016. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

**HEAD** Dr. K. Lakshmisudha  
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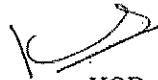


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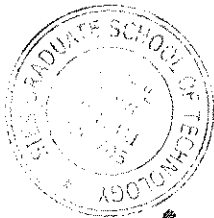
**15/8/2017**

All the students of BE IT are informed that we are organizing Latex Workshop for BE IT students 21 January 2017. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

**HEAD** Dr. K. Lakshmisudha  
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S. I. E. S. Graduate School of Technology  
Sri Chandrasekarendra Saraswathy Vidyapuram  
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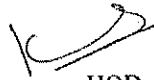
14. Augmented reality 18-02-2017-19-02-2017

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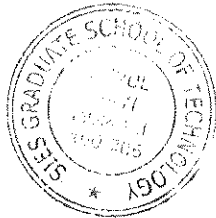
12/2/2017

All the students are informed that we are organizing Augmented Reality Workshop for all students from 18<sup>th</sup> February 2017 to 19<sup>th</sup> February 2017. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



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**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
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Sri Chandrasekarendra Saraswathy Vidyapuram  
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15. Project Selection 21/3/2017


Department of Information technology


Date :- 14/03/2017

Notice

2016-  
IT

All the Third-year students are hereby informed that a Seminar on "Guidelines for selecting Final Year Project" is scheduled on March 21, 2017 at 2:30 pm. All the students are requested to assemble in Room No. 112. Attendance is compulsory.

  
Prof. Neena Jacob  
Project Coordinator

  
14/03/17  
Prof. Leena Ladge  
HOD IT

2016



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HERKUL, NAVI, MUMBAI - 400 706


16.Soft skills for TE IT 3-30-2017

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DEPARTMENT OF INFORMATION TECHNOLOGY**

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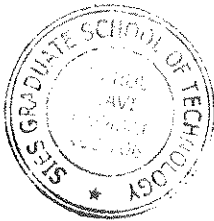
**24/3/2017**

All the students of TE IT are informed that we are organizing Soft Skill seminar for TE IT students on 30<sup>th</sup> March 2017. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

**HEAD Dr. K. Lakshmisudha**  
**Department of Information Technology**  
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**Sri Chandrasekarendra Saraswathy Vidyapuram**  
**Plot-1-C&E, Sector-V, Nerul, Navi Mumbai-400706**



**Principal**  
**S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY**  
**807, NAVE'S SIX ROAD, SECTOR-V, NAVI MUMBAI-400706**  
**SECTOR-V, NERUL, NAVI MUMBAI-400706**

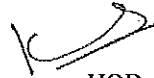
17. soft skills for TE IT 3-30-2017

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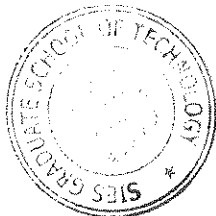
24/3/2017

All the students of FE IT are informed that we are organizing Soft Skill seminar for FE IT students on 30<sup>th</sup> March 2017. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

**HEAD** Dr. K. Lakshmisudha  
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S. I. E. S. Graduate School of Technology  
8/1 Chandrasekarendra Saraswathy Vidyapuram  
Plot-1-C&E, Sector-V, Nerul, Navi Mumbai-400706



Principal  
SIES GRADUATE SCHOOL OF TECHNOLOGY  
8/1 CHANDRASEKARENDRASARASWATHY VIDYAPURAM  
PLOT-1-C&E, SECTOR-V, NERUL, NAVI MUMBAI-400706

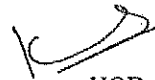
18. Storage Network management and retrieval 4/04/2017

**SIES GRADUATE SCHOOL OF TECHNOLOGY  
DEPARTMENT OF INFORMATION TECHNOLOGY**

**Notice**

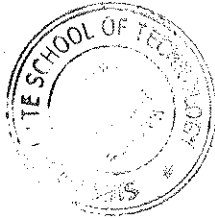
27/2/2017

All the students of BE IT are informed that we are organizing expert talk on Storage Network management and retrieval for BE IT students on 4<sup>th</sup> April 2017. Interested students can attend the session. Kindly coordinate with IETE chairperson for further details.



HOD

**HEAD** Dr. K. Lakshmisudha  
Department of Information Technology  
S. J. E. S. Graduate School of Technology  
9/1 Chondrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706



Principal  
S. J. E. S. Graduate School of Technology  
9/1 Chondrasekarendra Saraswathy Vidyapuram  
Plot-1-C & E, Sector-V, Nerul, Navi Mumbai-400706

## 37/19-20/Mech/Industrial projects and safety in Alfa Laval


Department of Mechanical Engineering

S.I.E.S. Graduate School of Technology, Nerul

03/05/2019

### NOTICE

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 09/05/2019 at 01:30 p.m. in the college Auditorium. The topic of seminar is "**Industrial projects and safety in Alfa Laval**". All the students should note that **attendance for seminar is compulsory.**

  
Dr. Pradip P Patil

SDP In-charge

  
Dr. Rupendra Nehete

HOD (Mech. Engg.)



**PRINCIPAL**  
S. I. E. S. GRADUATE SCHOOL OF TECHNOLOGY  
Sri Chandrasekarendra Saraswathy Vidyapuram  
Sector-V, Nerul, Navi Mumbai-400706



## 38/19-20/Mech/Sustainability in Project Management

Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

06/08/2019

#### NOTICE

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 10/08/2019 at 02:30 p.m. in the college Auditorium. The topic of seminar is “*Sustainability in Project Management*”. All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



**PRINCIPAL**  
S. I. E. S. GRADUATE SCHOOL OF TECHNOLOGY  
Sri Chandrasekharendra Saraswathy Vidyapuram,  
Sector-V, Nartoli, Navi Mumbai-400706.





**39/19-20/Mech/Project Management in Multinational Companies**



**S.I.E.S. Graduate School of Technology, Nerul**

03/05/2019

**NOTICE**

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 10/05/2019 at 02:30 p.m. in the college Auditorium. The topic of seminar is "*Project Management in Multinational Companies*". All the students should note that **attendance for seminar is compulsory.**

Dr. Pradip P Patil

SDP In-charge

Dr. Rupendra Nehete

HOD (Mech. Engg.)

PRINCIPAL  
S. I. E. S. GRADUATE SCHOOL OF TECHNOLOGY  
Sri Chandrababasaheb Sarabhai Vidyapeeth  
Sector-V, Nerul, Navi Mumbai-401306



## 40/19-20/Mech/Entrepreneurial awareness camp

Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

20/09/2019

#### NOTICE

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 27/09/2019, 28/09/2019 and 05/10/2019 at 01:30 p.m. in the college Auditorium. The topic of seminar is "*Entrepreneurial awareness camp*". All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. RupendraNehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SH. CHANDRASEK/RUPENDRA SARASWATHY VIDYAPURAM  
SECTOR-M, NERUL - 401 301, MUMBAI - 401 306.



## 69/18-19/Mech/Finite element analysis using Ansys Workbench

Department of Mechanical Engineering

S.I.E.S. Graduate School of Technology, Nerul

21/06/2018

### NOTICE

All the S.E. and T.E students of SIES GST are informed that the Mechanical department is organizing a 5 days hands on Training under SDP, will be held from 18/06/2018 to 22/06/2018 at Lab 05, Time 09:30 a.m to 4:30 p.m. The workshop title is "*Finite element analysis using Ansys Workbench*". All the registered students should note that **attendance for Certificate is compulsory.**

  
Dr. Pradip P Patil

SDP In-charge

  
Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/1/5, SIR CHANDRASEKHAR SARASWATHY VIDYAPURAM  
SECTOR-4, NERUL, NAVI MUMBAI - 401 706.



Department of Mechanical Engineering

✓

**S.I.E.S. Graduate School of Technology, Nerul**

21/06/2018

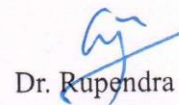
**NOTICE**

All the F.E. students of SIES GST are informed that the Mechanical department is organizing a 5 days Hands on Training under SDP, will be held from 25/06/2018 to 29/06/2018 at Lab 05, Time 10:00 a.m to 4:00 p.m. The workshop title is "***Drawing and drafting using Auto CAD***". All the registered students should note that **attendance for Certificate is compulsory**.



Dr. Pradip P Patil

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDRUSHEKHAR SARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, NAVE MUMBAI - 401 205.



## 71/18-19/Mech/Career guidance through Experience

Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

09/07/2018

#### NOTICE

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 13/07/2018 at 01:30 p.m. in the college Auditorium. The topic of seminar is "*Career guidance through Experience*". All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/D/15, S.I.E. COLONY, SANDHURD, VTDYAPURAM  
SECTOR-4, WARD-3, DIST. MUMBAI - 401 105.



## 72/18-19/Mech/Scope of Engineer in Solar Installation System

Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

26/07/2018

#### NOTICE

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 31/08/2018 at 02:30 p.m. in the college Auditorium. The topic of seminar is "*Scope of Engineer in Solar Installation System*". All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 10/W/E, SION CHAWI, 10TH FLOOR, SAKARWATHY VIDYAPURAM  
SECTOR-44, NERUL, EAST MUMBAI - 401 305.



73/18-19/Mech/Energy Saving

Department of Mechanical Engineering

**S.I.E.S. Graduate School of Technology, Nerul**

26/08/2018

**NOTICE**

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 29/08/2018 at 01:30 p.m. in the college Auditorium. The topic of seminar is "*Energy Saving*". All the students should note that **attendance for seminar is compulsory**.



Dr. Pradip P Patil

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 107/16, SIKH CHANDRASEKHAR NIKHIL SURKASWADHY VIDYAPURAM  
SECTOR-4, WARD-1, MUMBAI-401 305





**74/18-19/Mech/Communications and Ethics in project**


Department of Mechanical Engineering

**S.I.E.S. Graduate School of Technology, Nerul**

04/01/2019

**NOTICE**


All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on **08/01/2019 at 01:30 p.m.** in the college Auditorium. The topic of seminar is "*Communications and ethics in project*". All the students should note that **attendance for seminar is compulsory.**

  
Dr. Pradip Patil

SDP In-charge

  
Dr. Rupendra Nehete

HOD (Mech. Engg.)

  
Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 107/15, SIKR CHANDRAN PUSKAR, TILAKA SAMPASWATHY VIDYAPURAM,  
SECTOR-14, VIKTORIA, NEW TOWNSHIP, CHENNAI-600 095.



75/18-19/Mech/Aptitude development


Department of Mechanical Engineering

**S.I.E.S. Graduate School of Technology, Nerul**

14/01/2019

**NOTICE**

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 17/01/2019 at 01:30 p.m. in the college Auditorium. The topic of seminar is "*Aptitude development*". All the students should note that **attendance for seminar is compulsory.**

  
Dr. Pradip P Patil

SDP In-charge

  
Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDRASEKARENDRA SARASWATHY VIDYAPURAM  
SECTOR-N, NERUL, NAYI MUMBAI - 400 705.



## 76/18-19/Mech/Introduction to Ansys for Project Analysis

Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

25/01/2019

#### NOTICE

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 30/01/2019 at 02:30 p.m. in the college Auditorium. The topic of seminar is "*Introduction to Ansys for Project Analysis*". All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/S, SIK CHANDRASEKHARWADA SARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, MUMBAI - 401 205.



77/18-19/Mech/Life after Graduation


Department of Mechanical Engineering

**S.I.E.S. Graduate School of Technology, Nerul**

22/01/2019

**NOTICE**

All the B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 26/03/2019 at 01:30 p.m. in the college Auditorium. The topic of seminar is "*Life after Graduation*". All the students should note that **attendance for seminar is compulsory**.

  
Dr. Pradip P Patil

SDP In-charge

  
Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/5/B, SIR CHANDRASEKARENDRA SARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, NAVI MUMBAI - 400 706.



## 42/17-18/Mech/ Project Selection and Management

Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

06/07/2017

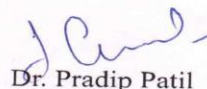
#### NOTICE

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on **13/07/2017 at 01:30 p.m.** in the college Auditorium. The topic of seminar is "**Project Selection and Management**". All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Pradip Patil

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/1/15, SIKKANDRASEKARENDRA SHYAMSWATHY VIDYAPURAM  
SECTOR 15, THERE, MAHARASHTRA - 401 205.



## 43/17-18/Mech/ Guidelines to Identify Project Topics

Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

09/04/2018

#### NOTICE

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 13/04/2018 at 02:30 p.m. in the college Auditorium. The topic of seminar is "*Guidelines to Identify Project Topics*". All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Pradip Patil

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/E/5, SIR CHINTAMSEKA KENDRA SAKESWATHY VIDYAPURAM  
SECTOR-V, 42<sup>ND</sup> FLR., NAVI MUMBAI - 401 215.



44/17-18/Mech/ Finite element analysis using Ansys Workbench

45/17-18/Mech/ Finite element analysis using Ansys Workbench

46/17-18/Mech/ Finite element analysis using Ansys Workbench

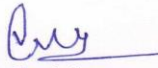
Department of Mechanical Engineering

**S.I.E.S. Graduate School of Technology, Nerul**

10/01/2018

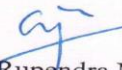
**NOTICE**

All the S.E. and T.E. Students of SIES GST are informed that a hands-on training under the SDP will be held on the following dates 20/01/2018, 3/02/2018 and 17/02/2018 in IT Lab-107 from 9:30 to 04:30. The topic of the seminar is "*Finite element analysis using Ansys Workbench*". All the registered students should note that **attendance for all the sessions is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIA CHIMBARSEK/RENISA SIKASWATHY VIDYAPURAM  
SECTOR 15, NERUL, MUMBAI - 401 305



## 47/17-18/Mech/Four stroke Internal Combustion Engine

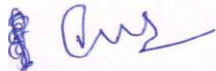
Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

25/01/2018


#### NOTICE

All the S.E. and T.E. Mechanical Engineering students are informed that a seminar under SDP will be held on 30/01/2018 at 01:30 p.m. in the college Auditorium. The topic of seminar is "*Four strokes Internal Combustion Engine*". All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDRASEKARENDRASARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, NAVI MUMBAI - 400 706.





## 48/17-18/Mech/Two stroke marine engines basic insights and concepts

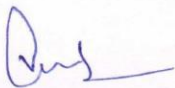
Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

23/02/2018

#### NOTICE

All the S.E. and T.E. Mechanical engineering students are informed that a seminar under SDP will be held on **28/02/2018 at 01:30 p.m.** in the **college Auditorium**. The topic of seminar is ***“Two stroke marine engines basic insights and concepts”***. All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



Principal  
**S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY**  
PLOT 1C/D/E, SIR CHANDRASEKARINORA SARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, NAVI MUMBAI - 401 706.



49/17-18/Mech/ Electrical and Engine Automation

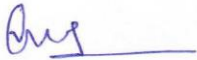
Department of Mechanical Engineering

**S.I.E.S. Graduate School of Technology, Nerul**

19/03/2018

**NOTICE**

All the T.E. and B.E. students of Mechanical engineering are informed that a seminar under SDP will be held on **23/03/2018 at 02:30 p.m.** in the **college Auditorium**. The topic of seminar is "**Electrical and Engine Automation**". All the registered students should note that **attendance for all the sessions is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Rupendra Nehete

HOD (Mech. Engg.)



**Principal**  
**S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY**  
PLOT 1C/D/E, SIR CHANDRASEKHARJI LINGNA SAKRASWATHY VIDYAPURAM  
SECTOR-V, NERUL, DIST. NARRAYAN - 401 206.



## 19/16-17/Mech/Modelling and Simulation of Metal Casting

Department of Mechanical Engineering

### S.I.E.S. Graduate School of Technology, Nerul

3/02/2017

#### NOTICE

All the S.E. and T.E. students of Mechanical engineering are informed that a seminar under SDP will be held on **07/03/2017 at 02:30 p.m.** in the college Auditorium. The topic of seminar is "**Modelling and Simulation of Metal Casting**". All the registered students should note that **attendance for all the sessions is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Pradip Patil

HOD (Mech. Engg.)



Principal

S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDESEKHARJIKA SARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, NAVE MURUBAI - 401 705.



19/16-17/Mech/Unconventional And Micro-Manufacturing

Department of Mechanical Engineering

S.I.E.S. Graduate School of Technology, Nerul

15/02/2017

NOTICE

All the T.E. and B.E. Mechanical Engineering students are informed that a seminar under SDP will be held on **20/02/2017 at 03:30 p.m.** in the college Auditorium. The topic of seminar is **"Unconventional And Micro-Manufacturing"**. All the students should note that **attendance for seminar is compulsory.**



P.K. Ambadekar

SDP In-charge



Dr. Pradip Patil

HOD (Mech. Engg.)



Principal  
S.I.E.S. GRADUATE SCHOOL OF TECHNOLOGY  
PLOT 1C/D/E, SIR CHANDRASEKHAR TEJVARAY SARASWATHY VIDYAPURAM  
SECTOR-V, NERUL, (NVT MUNICIPAL - 400) 705.

